PSN COLLEGE OF ENGINEERING AND TECHNOLOGY (Autonomous)

Melathediyoor, Palayamkottai (TK), Tirunelveli (DT), Pin: 627 152

Department of Mechanical Engineering

CONTINOUS ASSESSMENT TEST CELL

CONTINUOUS ASSESSMENT TEST - II (February 2020)

TIME TABLE

CLASS: II YEAR/ 4rd Semester

DEPT	3.2.2020 (FN)	4.2.2020 (FN)	5.2.2020 (FN)	6.2.2020 (FN)	7.2.2020 (FN)	8.2.2020 (FN)
	9.30 am TO 11.00 am	9.30 am TO 11.00 am	9.30 am TO 11.00 am	9.30 am TO 11.00 am	9.30 am TO 11.00 am	9.30 am TO 11.00 am
AERO	Engineering Mathematics III (EM-III)	Aerodynamics I	Aircraft Structures I	Control Engineering	Propulsion I	Aircraft systems and Instruments
CIVIL	Engineering Mathematics III (EM-III)	Soil Mechanics	Mechanics of Materials	Fluid Mechanics and Machinery	Engineering Survey II	Tranportation Engineering– II
CSE	Engineering Mathematics III (EM-III)	Computer Networks (CN)	Design and Analysis of Algorithms (DAA)	Java Programming (JP)	Software Engineering (SE)	Microcontroller and Embedded Systems (MCES)
EEE	Engineering Mathematics -III	Electrical Machines -II(EM – II)	Control System(Practical Component)(CS)	Transmission and Distribution(T & D)	Conventional and Non-Conventional Energy sources(C&NCES)	Linear Integrated and Digital Logic Circuits (LIC)
ECE	Engineering Mathematics II	Electronics Circuits	Transmission Lines and Wave Guides	Control System	Signals and System	Linear Integrated Circuits
EIE	Engineering Mathematics III	Industrial Instrumentation I	Transducer Engineering	Thermodynamics and Fluid Mechanics	Analog Integrated Circuits	Object Oriented Programming
MARINE	Non Destructive Testing	Naval Architecture – I	Ship Structure & Construction	Marine Electronics	Marine Hydraulics & Machineries	AC Electrical Machines
M & A	Engineering Mathematics – III	Mechanics of Machines	Production Technology II	Hydraulics and Pneumatics	LAN and Network	Electrical Drives and Control
MECH	Engineering Mathematics – III	Strength of Materials	Theorys Of Machines I	Thermal Engineering	Applied hydraulics and Pneumatics	Manufacturing Technology II

Exam Cell Coordinator

Principal

PSN COLLEGE OF ENGINEERING & TECHNOLOGY * MELATHEDIYOOR * TIRUNELVEL DISTRICT 627 152

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PSN COLLEGE OF ENGINEERING AND TECHNOLOGY (Autonomous)

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Department of Mechanical Engineering

CONTINOUS ASSESSMENT TEST CELL

CONTINUOUS ASSESSMENT TEST - II (February 2020)

TIME TABLE

CLASS : III YEAR / 6th Semester

DEPT	3.2.2020 (FN)	4.2.2020 (FN)	5.2.2020 (FN)	5.2.2020 (AN)	6.2.2020 (FN)	7.2.2020 (FN)	8.2.2020 (FN)
	9.30 am to 11.00 am	9.30 am to 11.00 am	9.30 am to 11.00 am	3.00 Pm to 04.30	9.30 am to 11.00 am	9.30 am TO 11.00 am	9.30 am to 11.00 am
AERO	Avionics	Flight Dynamics	Heat Transfer		Finite element analysis	Aircraft general engineering and maintanance	Non Destructive Examination
CIVIL	Principles of Management	Structural Analysis – II	Design of steel structures		Construction Planning and Scheduling	ENVIRONMENTAL ENGINEERING	POWER PLANT ENGINEERING **(OPE)**
CSE	Discrete Mathematics	Object Oriented Analysis and Design	Internet Programming (Common to IT)		Cloud Computing	Compiler Design (Practical Component)	Cellular and Mobile Communication (Common to IT)
EEE	High Voltage DC Transmission(HVDC)	Power System Operation and Control(PSOC)	Microprocessor and Microcontroller(μP)		Design of Transmission systems	Computer Network(Elective –I)(CN)	Non Destructive Testing
ECE	Digital Image Processing	Digital Communication	VLSI Design		Computer and Communication Networks	Wireless communication	JAVA programming
EIE	Fiber Optics and Laser Instrusments	Process Control	Microprocessor and Microcontroller		Digital Signal Processing(DSP)	Industrial Electronics	Power Plant Engineering
IT	Discrete Mathematics	Object Oriented Analysis and Design	Internet Programming (Common to IT)		Service Oriented Architecture (Practical Component)	Tele Communication System	Cellular and Mobile Communication (Common to IT)
MARINE	Ship Fire Prevention & Control	Marine Internal Combustion Engine -II	Marine Electrical Technology	Environmental Impact Assessment (Open Elective - II)	Naval Architecture-I	Marine Auxiliary Machine-II	Offshore Technology(elective Paper - II)
M & A	Operation research	Design of Jigs and Fixtures	Computer integrated manufacturing		Fluid power transmission system	Heat and Mass transfer	Non Destructive Examination
MECH	Gas Dynamics and Get Propulsion	Quality Control and Reliability Engineering	Automobile Engineering		Disaster Management	Design of Transmission systems	Power Plant Engineering

Exam Cell Coordinator

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Department of Mechanical Engineering

CONTINOUS ASSESSMENT TEST CELL

CONTINUOUS ASSESSMENT TEST - II (February 2020)

TIME TABLE

CLASS: IV YEAR / 7th Semester

DEPT	4.2.2020 (FN)	5.2.2020 (FN)	6.2.2020 (FN)
	9.30 am to 11.00 am	9.30 am to 11.00 am	9.30 am to 11.00 am
AERO	Rocket and Missles	Project Management	Airtraffic control and Planning
CIVIL	Prefabricated structures	Repair And Rehabilitation Of Structures	Dynamics and Seismic Design of Structures
CSE	Software Project Management (Common To IT)	Information Retrieval Techniques (Common to IT)	Human Computer Interface (Common to IT)
EEE	Conservation and Utilization of Electrical Energy(CUEE)	Smart grid(SG)	Operating System(OS)
ECE	Wireless Networks	Fuzzy Logic Control	
EIE	Power Plant Instrumentation	SCADA and HMI	
IT	Software Project Management (Common To IT)	Information Retrieval Techniques (Common to IT)	Human Computer Interface (Common to IT)
M & A	Production Planning and Control	Maintanance Engineering	
MECH	Production Planning and Control	Maintanance Engineering	

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CONTINUOUS ASSESSMENT TEST CELL

28.01.2020

CIRCULAR

It is planned to conduct the **Continuous Assessment Test - II** from 03.02.2020 – 08.02.2020 for II, III and IV Year U.G students of all branches of our college. Hence the faculty members who are handling the subjects [II, III & IV Year U.G] are requested to prepare and send the hard and soft copy of question paper to **Continuous Assessment Test Cell** on or before 30.01.2020. The syllabus for the **Continuous Assessment Test - II** is **1.5 Units** and the duration of the exam is **1hr 30min**. The question paper should be in uniform format for all subjects and total mark is 50 with below mentioned split up:

DIVISION	TOTAL NO OF QUESTION	MARK FOR EACH QUESTION	TOTAL MARKS
PART A	5	2	10
PART B	1(with Choice)	8	8
PART C	2 (with Choice)	16	32
Grand Total			**50**

Note:

- All the HODs are requested to ensure each question paper whether it covers all portions of the last Units and no replication of questions.
- Send all soft copies of question papers of each department to e-mail id: psncetcat2019@gmail.com on or before 30.01.2020 and the question papers will not be accepted after the above mentioned date.
- The soft copies of the question papers should be sent through their respective department e-mail id.
- The hard Copies of the question paper should be sent to the exam cell with approval from Head of the Department.

Exam Cell Coordinator **Principal**

PSN COLLEGE OF ENGINEERING & TECHNOLOGY TIRUNELVELI DISTRICT 627 152

To

Principal, Executive Director, Director Academics
HODs (Aero,Civil,CSE,ECE,EEE,EIE,IT,Marine,MECH,M&A)

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CONTINUOUS INTERNAL ASSESSMENT – II

B.E……	Department:	Year/Semester:
Date & Session:	Course name and Code	Max. Marks:50

Course Outcomes (COs):

CO1:

CO2:

...

BL – *Bloom's Level (1- Remembering, 2- Understanding, 3 – Applying, 4 – Analysing, 5 – Evaluating, 6 - Creating)*; **CO** – *Course Outcome*;

Qn. No.	PART A (2 marks)	Marks	CO	BL
1				
2				
3				
4				
5				
Qn. No.	**PART B (8 marks)**	**Marks**	**CO**	**BL**
6(a)				
	(or)			
6(b)				
Qn. No.	**PART B (16 marks)**	**Marks**	**CO**	**BL**
7(a)				
	(or)			
7(b)				
8(a)				
	(or)			
8(b)				

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CONTINUOUS ASSESSMENT TEST – II

B.E	Department: ECE	Year/Semester: II/IV
Date & Session: 04.02.2020 FN	Course name and Code: Electronic Circuits	Max. Marks:50

Course Outcomes (COs):

CO2: The student will be able to discuss the frequency response analysis of BJT and FET amplifiers and to obtain the upper and lower cutoff frequency.

CO3: Derive the negative feedback amplifiers parameters and also discuss the various types of oscillators and its operation

BL – *Bloom's Level (1- Remembering, 2- Understanding, 3 – Applying, 4 – Analyzing, 5 – Evaluating, 6 – Creating)*; **CO** – *Course Outcome*;

Qn. No.	PART A (2 marks)	Marks	CO	BL
1	Define Darlington amplifier	2	CO-2	BL-1
2	Compare the differential and common mode gain.	2	CO-2	BL-2
3	Define Hartley oscillator	2	CO-3	BL-1
4	Convert the general feedback structure into positive feedback structure	2	CO-3	BL-2
5	List out the conditions of an oscillator	2	CO-3	BL-1
Qn. No.	**PART B (8 marks)**	**Marks**	**CO**	**BL**
6(a)	Discuss the different types of amplifier circuits.	8	CO-3	BL-2
	(or)			
6(b)	Explain the working of tuned collector and clapp oscillator	8	CO-3	BL-2
Qn. No.	**PART B (16 marks)**	**Marks**	**CO**	**BL**
7(a)	Analyze the methods to improve the input impedances.	16	CO-2	BL-4
	(or)			
7(b)	Evaluate the performance of emitter coupled differential amplifier.	16	CO-2	BL-4
8(a)	Construct the voltage series amplifier circuit.	16	CO-3	BL-3
	(or)			
8(b)	Explain the Colpitts oscillator	16	CO-3	BL-3

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EXAM CELL

HALL: BAY 1 & 2

C1	C2	C3	C4	C5
1708001	1808001	1708011	1808012	1708022
1708002	1808002	1708012	1808013	1708023
1708003	1808003	1708013	1808014	1708024
1708004	1808004	1708014	1808015	1708025
1708005	1808006	1708015	1808016	1708026
1708006	1808007	1708017	1808017	1708027
1708007	1808008	1708018	1808018	1708028
1708008	1808009	1708019	1808301	1708029
1708009	1808010	1708020	1808302	1708031
1708010	1808011	1708021	1808303	1708033

S.No	Reg.No	Dept Name	No.of Students
1	1708001-15,17-29,31,33	III MARINE	30
2	1808001-4,6-18,301-303	II MARINE	20
		Total	50

Exam Cell Co-ordinator

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Principal

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EXAM CELL

HALL: BAY 3 & 4

English for Mariners

C1	C2	C3	C4	C5
1708034	1808304	1708044	1801005	1701001
1708035	1808305	1708301	1801006	1701002
1708036	1808306	1708302	1801008	1701003
1708037	1808307	1708303	1801009	1701004
1708038	1808308	1708304	1801010	1701005
1708039	1808309	1708305	1801012	1701006
1708040	1808310	1708306	1801013	1701007
1708041	1801002	1708307	1801015	1701009
1708042	1801003	1708309	1801016	1701011
1708043	1801004	1708310	1801019	1701013

S.No	Reg.No	Dept Name	No.of Students
1	1708034-44,301-307,309,310	III MARINE	20
2	1808304-310	II MARINE	7
3	1801002-6,8-10,12,13,15,16,19	II AERO	13
4	1701001-7,9,11,13	III AERO	10
		Total	50

Exam Cell Co-ordinator

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EXAM CELL

HALL: BAY 5 & 6

C1	C2	C3	C4	C5
1801021	1801035	1701014	1704006	1701031
1801022	1801037	1701015	1704007	1701033
1801023	1801040	1701016	1704008	1701034
1801026	1801043	1701018	1704009	1701035
1801027	1801044	1701019	1704010	1701036
1801028	1801045	1701020	1704011	1701037
1801029	1704001	1701021	1704012	1701301
1801030	1704002	1701023	1704013	1704017
1801031	1704003	1701026	1704014	1704018
1801033	1704004	1701030	1704016	1704019

S.No	Reg.No	Dept Name	No.of Students
1	1801021-23,26-31,33,35,37,40,43,44,45	II AERO	16
2	1701014-16,18-21,23,26,30,31,33-37,301	IIIAERO	17
3	1704001-4,6-14,16-19	III EEE	17
		Total	50

Exam Cell Co-ordinator

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EXAM CELL

HALL: BAY 7 & 8

C1	C2	C3	C4	C5
1704020	1804001	1704034	1804022	*1802002*
1704021	1804002	1704035	1804023	*1802003*
1704022	1804003	1704037	1804025	*1802004*
1704023	1804006	1704038	1804026	*1802006*
1704028	1804007	1704041	1804301	*1802009*
1704029	1804012	1704302	1804304	*1802010*
1704030	1804016	1704303	1804305	*1802302*
1704031	1804017	1704304	1804306	*1802303*
1704032	1804020	1704305	1804307	*1802304*
1704033	1804021	1704306	1804309	*1802305*

English for Mariners

S.No	Reg.No	Dept Name	No.of Students
1	1704020-23,28-35,37,38,42,302-306	III EEE	20
2	1804001-3,6,12,16,17,20-23,25,26,301,304,305-307,309	II EEE	20
3	1802002-4,6,9,10,302,303,304,305	II CIVIL	10
		Total	50

Exam Cell Co-ordinator

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EXAM CELL

HALL: BAY 9 & 10

C1	C2	C3	C4	C5
1702005	1705002	1702302	1703004	1805020
1702006	1705005	1702303	1703006	1805021
1702011	1705008	1702304	1703008	1805023
1702012	1705009	1702305	1703012	1703301
1702013	1705013	1805001	1703014	1703304
1702015	1705015	1805002	1703015	
1702017	1705016	1805004	1703016	
1702024	1705301	1805005	1703017	
1702026	1705302	1805006	1703024	
1702301	1703001	1805007	1703025	

S.No	Reg.No	Dept Name	No.of Students
1	1702005,6,11-13,15,17,24,26,301-305	III CIVIL	14
2	1805001,2,4-7,21,23	II ECE	9
3	1705002,5,8,9,13,15,16,301,302	III ECE	9
4	1703001,4,6,8,12,14-17,24,25,301,304	III CSE	13
		Total	45

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EXAM CELL

HALL: BAY 11 & 12

C1
1803006
1803008
1803010
1803013
1803020
1803025
1803026
1803030
1803033
1803035

C2
1605001
1605006
1605012
1605017
1605019
1605020
1605021
1605022
1605301
1605302

C3
1803039
1803040
1803041
1803043
1803302
1803303
1803304
1803305

C4
1605304
1603009
1603011
1603015
1603017
1603021
1603026
1603028
1603029

English for Mariners

C5
1806001
1806003
1806004
1806005
1806006
1806007
1806301
1706001

S.No	Reg.No	Dept Name	No.of Students
1	1803006,08,10,13,20,25-26,30,33,35,39-41,43,302-305	II CSE	18
2	1806001,3,4,5,6,7,301	II EIE	7
3	1706001	III EIE	1
4	1605001,6,12,17,19-22,301,302,304	IV ECE	11
5	1603009,11,15,17,21,26,28,29	IV CSE	8
		Total	45

Exam Cell Co-ordinator

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EXAM CELL

HALL: BAY 13 & 14

C1
1601001
1601003
1601005
1601006
1601007
1601008
1601012
1601013
1601016
1601017

C2
1604001
1604002
1604004
1604006
1604007
1604008
1604010
1604012
1604013
1604014

C3
1601020
1601025
1601026
1601027
1601029
1601031
1601032
1601033
1601301
1604015

C4
1604016
1604017
1604019
1604023
1604024
1604026
1604029
1604030
1604031
1604034

C5
1604301
1604303
1604304
1604305
1604306
1604307
1606001
1606002
1606006
1607003
1607007

S.No	Reg.No	Dept Name	No.of Students
1	1604001,24,6-8,10,12-17,19,23,24,26,29-31,34,301,303-307	IV EEE	27
2	1606001,2,6	IV EIE	3
3	1607003,7	IV IT	2
4	1601001,3,5-8,12,13,16,17,20,25-27,29,31-33,301	IV AERO	19
		Total	51

Exam Cell Co-ordinator

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EXAM CELL

HALL: BAY 15 & 16

C1	C2	C3	C4	C5
1602002	1609055	1602019	*1610310*	1602037
1602005	1609056	1602021	*1610311*	1602038
1602008	1609057	1602022	*1610312*	1602039
1602009	1609058	1602023	*1610313*	1602040
1602011	1609059	1602025	*1610314*	1602041
1602012	1609060	1602026	*1610315*	1602301
1602013	1609301	1602027	*1610316*	1602302
1602015	1609303	1602028	*1610317*	1602304
1602017	*1610306*	1602029	1602032	1602305
1602018	*1610307*	1602030	1602033	1602306

S.No	Reg.No	Dept Name	No.of Students
1	1610306-317	IV MECH	10
2	1609055-60,301,303	IV MAE	8
3	1602002,5,8,9,11-13,15,17-19,21-23,25-30,32,33,37-41,302,304-306	IV CIVIL	32
		Total	**50**

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EXAM CELL

HALL: II MECH

C1	C2	C3
1709002	1810002	1709008
1709003	1810003	1709009
1709004	1810004	1709010
1709007	1810007	1709011

C4	C5	C6
1810008	1709013	1810013
1810009	1709014	1810014
1810010	1709015	1810015
1810011	1709016	1810017

C7	C8	C9
1709017	1810019	1709022
1709018	1810020	1709023
1709019	1810021	1709024
1709020	1810025	1709026

S.No	Reg.No	Dept Name	No.of Students
1	1810002-4,7-11,13-15,17,19-21,25	II MECH	16
2	1709002-4,7-11,13-20,22-24,26	III MAE	20
		Total	36

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EXAM CELL

HALL: III MECH

C1	C2	C3
1810026	1709028	1810030
1810027	1709029	1810031
1810028	1709030	1810032
1810029	1709031	1810033

C4	C5	C6
1709032	1810034	1709036
1709033	1810301	1709037
1709034	1810302	1709039
1709035	1810303	1709040

C7	C8	C9
1810304	1709041	1810308
1810305	1709042	1810309
1810306	1709045	1810310
1810307		1810311
		1810312
		1810313

S.No	Reg.No	Dept Name	No.of Students
1	1810026-34,301-313	II MECH	22
2	1709028-37,39-42,45	III MAE	15
		Total	37

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EXAM CELL

HALL: II MAE

C1	C2	C3
1710002	1809001	1710010
1710003	1809002	1710011
1710005	1809003	1710013
1710006	1809004	1710014
1710007	1809005	1710016
1710008	1809006	1710018

C4	C5	C6
1809007	1710020	1809014
1809009	1710021	1809015
1809010	1710025	1809016
1809011	1710026	1809017
1809012	1710030	1809019
1809013	1710032	1809020

S.No	Reg.No	Dept Name	No.of Students
1	1710002-32	III MECH	18
2	1809001-7,9-20	II MAE	18
		Total	36

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EXAM CELL

HALL: IV MECH

C1	C2	C3
1610029	1609001	1610034
1610030	1609002	1610035
1610031	1609003	1610036
1610032	1609004	1610037
1610033	1609005	1610039

C4	C5	C6
1609006	1610040	1609013
1609007	1610041	1609014
1609009	1610043	1609015
1609010	1610045	1609016
1609012	1610047	1609018

C7	C8	C9
1610048	1609019	1610053
1610049		1610054
1610050	1610303	1610055
1610051	1610305	1610056
1610052		1610302

S.No	Reg.No
1	1610029-305
2	1609001-7,9,10,12-16,18,19

Dept Name	No.of Students
IV MECH	27
IV MAE	16
Total	43

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EXAM CELL

HALL: III MAE

C1	C2	C3
1710033	1809021	1710041
1710034	1809022	1710043
1710035	1809023	1710044
1710037	1809024	1710045
1710038	1809025	1710047
1710039	1809026	1710048

C4	C5	C6
1809027	1710049	1809302
1809028	1710050	1809303
1710051	1809029	1710303
1710301	1809030	1710304
1710302	1809301	1710305
		1710306
		1710309

S.No	Reg.No	Dept Name	No.of Students
1	1710033-309	III MECH	22
2	1809021-30,301-303	II MAE	13
		Total	35

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EXAM CELL

HALL: IV MAE

C1	C2	C3
1609020	1610002	1609025
1609021	1610003	1609026
1609022	1610004	1609027
1609023	1610005	1609028
1609024	1610006	1609029

C4	C5	C6
1610009	1609031	1610016
1610010	1609032	1610017
1610011	1609033	1610018
1610012	1609034	1610019
1610013	1609038	1610020

C7	C8	C9
1609041	1610021	1609049
1609043	1610022	1609050
1609045	1610025	1609051
1609046	1610026	1609052
1609048	1610028	1609054

S.No	Reg.No
1	1609020-29,31-34,38,41,43,45,46,48-52,54
2	161002-28

Dept Name	No.of Students
IV MAE	25
IV MECH	20
Total	45

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EXAM CELL

HALL: II Civil

C1	C2	C3
1801001	*1703003*	**1801014**
1801007	*1703005*	**1801017**
1801011	*1703009*	**1801018**

C4	C5	C6
1703010	**1801020**	*1703018*
1703011	**1801025**	*1703019*
1703013	**1801032**	*1703021*

C7	C8	C9
1801034	*1703022*	**1801042**
1801036	*1703023*	*1707003*
1801038	*1703302*	*1707004*
1801041	*1703303*	**1806002**

S.No	Reg.No	Dept Name	No.of Students
1	1801001,7,11,14,17,18,20,25,32,34,36,38,41,42	II AERO	14
2	1707003,4	III IT	2
3	1806002	II EIE	1
4	1703003,5,9,10,,11,13,18,19,21-23,302,303	III CSE	13
		Total	30

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EXAM CELL

HALL: III Civil

C1	C2	C3
1803001	1804004	1803004
1803002	1804005	1803007
1803003	1804008	1803011

C4	C5	C6
1804009	1803012	1804013
1804010	1803017	1804014
1804011	1803018	1804018

C7	C8	C9
1803019	1804019	1803021
1803014	*1702007*	1803022
1803015	*1702020*	1803023
1803016	*1702028*	

S.No	Reg.No	Dept Name	No.of Students
1	1702007,20,28	III CIVIL	3
2	1803001-4,7,11,12,17-19,14-16,21-23	II CSE	16
3	1804004,5,8,9,10,11,13,14,18,19	II EEE	10
		Total	29

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HALL: IV Civil

C1	C2	C3
1602024	1603001	1605003
1602036	1603007	1605004
1601009	1603010	1605005
1601014	1603012	1605007
1605002	1603013	1605008

C4	C5	C6
1603014	1605009	1603027
1603016	1605010	1603032
1603019	1605011	1607006
1603020	1605013	1607008
1603025	1605014	1607010

C7	C8	C9
1605015		1605027
1605023		1604009
1605024		1604011
1605025		1604027
1605026		1604038

S.No	Reg.No	Dept Name	No.of Students
1	1602024,36	IV CIVIL	2
2	1601009,14	IV AERO	2
3	1603001,7,10,12-14,16,19,20,25,27,32	IV CSE	12
4	1607006,8,10	IV IT	3
5	1605002-5,7-11,13,14,15,23-27	IV ECE	17
6	1604009,11,27,38	IVEEE	4
		Total	40

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EXAM CELL

HALL: III EEE

C1	C2	C3
1701008	**1803024**	1701025
1701010	**1803028**	1701028
1701012	**1803029**	1701029
1701017	**1803032**	1701032

C4	C5	C6
1805008	**1803034**	**1805013**
1805009	**1803036**	**1805014**
1805010	**1803037**	**1805015**
1805011	**1803042**	**1805016**
1805012	*1705001*	**1805017**

C7	C8	C9
1705003	**1805019**	1704005
1705004	**1805022**	1704024
1705006	**1805301**	1704025
1705007	1704039	*1705012*
1705011	1704040	*1705014*
		1704301

S.No	Reg.No	Dept Name	No.of Students
1	1701008,10,12,17,25,28,29,32	III AERO	8
2	1803024,28,29,32,34,36,37,42	II CSE	8
3	1805008-13-17,19,22,301	II ECE	13
4	1705001,3,4,6,7,11,12,14	III ECE	8
5	1704005-24,25,39,40,301	III EEE	6
		Total	43

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EXAM CELL

Date: 28.01.2020

FACULTY INVIGILATION DUTY DETAILS

Sl o	DEPT	STAFF NAME	DATE						
			3.2.2020 (9.30-11.00)	4.2.2020 (9.30-11.00)	5.2.2020 (9.30-11.00)	6.2.2020 (9.30-11.00)	7.2.2020 (9.30-11.00)	8.2.2020 (9.30-11.00)	8.2.2020 (3.00-4.30)
1	AERO	M.GODWIN JOE					III EEE	II CIVIL	
		Mr K.ESAKKIMUTHU	BAY 1&2	BAY 5&6					
		V.SHENBAGAMUTHURAMA			BAY 1&2			III EEE	
		Mr V.SEETHARAM				BAY 1&2		BAY 1&2	
		Mr JITHIN MALAYIL JACOB	BAY 13&14		II MAE				
		Mr M.EKAMBARAM		BAY 7&8	III CIVIL				
2	CIVIL	Dr.K.CHANDRASEKAR	BAY 9&10		BAY 5&6				
		Mr. B. MUTHURAMU	BAY 7&8	II MECH					
		Mr.G.PAULPANDI	BAY 3&4	III CIVIL					
		Mr.M.ANANDH BABU				III CIVIL			
		Mrs. A. NIVAS NISHA			BAY 7&8			BAY 5&6	
		Ms.S. MAHALAKSHMI	BAY 11 &12			BAY 5&6			
3	CSE	Dr. G. NALLASIVAN				II MAE	BAY 5&6		
		Mr. G.KHARMEGA SUNDARARAJ							
		Mr. R. MADHAVAN			II EEE		III CIVIL		
		Dr. M. SANTHANA KUMAR							
		Mr. M. VELLADURAI	BAY 5&6	BAY 3&4					
		Mr. A. SIVA		II MAE		BAY 7&8			
		Mr. C. RAJESH KUMAR	BAY 15&16	BAY 1&2					
		Mrs. P. KANAGALAKSHMI	II MECH	BAY 11 &12					
		Mrs. K. MONICA	III MECH				II MECH		
		Mrs. J. YAMUNA BEE			BAY 11 &12		BAY 7&8		
		Mr. Y. PEER MOHAMMED	III EEE	BAY 13&14					
		Mrs. JAINUL FATHIMA	IV MECH					III MECH	

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4	ECE	Dr.T.RAJESH	II CIVIL	IV MECH				BAY 9&10	
		Mr.K.JEBASTIN		III EEE					
		Mrs. M.SUNDARA			III EEE	BAY 11 &12			
		Mr.G.ESWARAN	III CIVIL			III EEE			
		Ms.K.ANITHA			IV MECH			III CIVIL	
		Ms.J.JEBALIN MAHIBA			III MECH			II MAE	
		Ms.A.CHIERA	IV CIVIL					BAY 3&4	
		Mr.MUTHUPONARASU		CIVIL D/H			BAY 1&2		
5	EEE	MS. V. JENITHA			II MECH		BAY 11 &12		
		Mr.S.AMOSEDINAKARAN	II EEE				III MECH		
		Mrs. J. LEEMA ROSE		II EEE				BAY 11 &12	
		Ms.P.MUTHULAKSHMI				III MECH			
6	EIE	Mr S. JESUHARAN SAMUEL			BAY 9&10		II MAE		
7	M&A	Mr.M.THIRUKUMARAN		BAY 9&10					
		Mr K.SARAVANA			II CIVIL		II CIVIL		
		Mr.K.SUDHAKAR		II CIVIL	BAY 13&14				
		Mr.S.R.STALIN	CIVIL D/H	III MECH					
		Mr.S.A.A.JUDE		IV CIVIL		II MECH			
		Mr.K.MUTHUKUMAR				II EEE		II MECH	
8	Marine	Mr.A C MARIAPPAN	II MAE				II EEE		
		Mr. M SURESH				II CIVIL	BAY 3&4		
		Mr.N.ANISH KAFOOR				BAY 3&4		BAY 7 & 8	
		Mr. C INDRAKUMAR						II EEE	III MARINE
		Mr.M MURUGAN		BAY 15&16		BAY 9&10			
		Mr.S.ARUNKUMAR			BAY 3&4		BAY 9&10		

The faculty members are expected to attend the examination duty promptly and report to the examination cell (Mechanical Engineering Department – formerly new first year block) 20 minutes before the scheduled time. They should not alternate their duty on their own. In case of urgency of they have to make alternate arrangement after taking due concurrence of Exam Cell Coordinator (Dr N.Vijay Ponraj) or HOD/ Mech. All the HODs are requested to extend full cooperation to conduct Continuous Assessment Test smoothly and also see that their staffs are attending the examination duty in time.

Exam Cell Coordinator

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CONTINUOUS INTERNAL ASSESSMENT – II

B.E	Department: ECE	Year/Semester: II/IV
Date & Session:	Course name and Code: Electronic Circuits	Max. Marks:50

Course Outcomes (COs):

CO2: The student will be able to discuss the frequency response analysis of BJT and FET amplifiers and to obtain the upper and lower cutoff frequency.

CO3: Derive the negative feedback amplifiers parameters and also discuss the various types of oscillators and its operation

BL – *Bloom's Level (1- Remembering, 2- Understanding, 3 – Applying, 4 – Analyzing, 5 – Evaluating, 6 – Creating)*; **CO** – *Course Outcome*;

Qn. No.	PART A (2 marks)	Marks	CO	BL
1	Define Darlington amplifier	2	CO-2	BL-1
2	Compare the differential and common mode gain.	2	CO-2	BL-2
3	Define Hartley oscillator	2	CO-3	BL-1
4	Convert the general feedback structure into positive feedback structure	2	CO-3	BL-2
5	List out the conditions of an oscillator	2	CO-3	BL-1
Qn. No.	**PART B (8 marks)**	**Marks**	**CO**	**BL**
6(a)	Discuss the different types of amplifier circuits.	8	CO-3	BL-2
	(or)			
6(b)	Explain the working of tuned collector and clapp oscillator	8	CO-3	BL-2
Qn. No.	**PART B (16 marks)**	**Marks**	**CO**	**BL**
7(a)	Analyze the methods to improve the input impedances.	16	CO-2	BL-4
	(or)			
7(b)	Evaluate the performance of emitter coupled differential amplifier.	16	CO-2	BL-4
8(a)	Construct the voltage series amplifier circuit.	16	CO-3	BL-3
	(or)			
8(b)	Explain the Colpitts oscillator	16	CO-3	BL-3

Faculty in Charge

HoD/ECE

PROF AND HEAD
Department Of ECE
PSN College Of Engg & Tech
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Tirunelveli - 627 152

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CAT - II

ANSWER KEY

PART-A.

1) Darlington Amplifier: (2 marks)

* The darlington transistor is made up of two PNP (or) NPN BJT's by connecting together.

* To improve the input impedance.

* The emitter of the PNP transistor is connected to the base of the other PNP transistor to create a sensitive transistor with high current gain used in many application.

(2 marks)

2) Differential and common mode gain.

Differential mode gain	Common mode gain.
(i) The gain ~~with~~ with which it amplifiers the difference between two input signal is called differential	(i) The gain with which it amplifies the common mode signal to produce the output is called

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gain of the differential amplifier	common mode gain.
(ii) $A_d = V_o / V_d$	(ii) $A_c = V_o / V_c$
(iii) $V_d = (V_1 - V_2)$	(iii) $V_c = \frac{V_1 + V_2}{2}$
(iv) High differential voltage gain.	(iv) Low common mode gain.

3). <u>Hartley Oscillator:</u> (2 marks)

* Hartley oscillator is a LC oscillator

* which uses two inductive reactance and one capacitive reactance in its feedback network

4)

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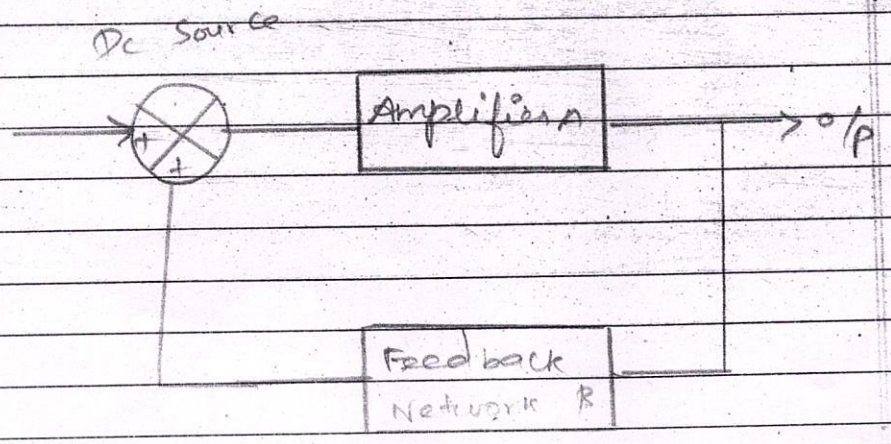
Date

A) General feedback Structure into positive feedback structure.

Circuit Diagram (2 marks)



b) Conditions of an oscillator:

Explanation - 2 marks)

* The ~~phase~~ total phase shift in the closed loop is 0° or 360°

* The magnitude of the loop gain is $|A\beta| = 1$

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PART - B.

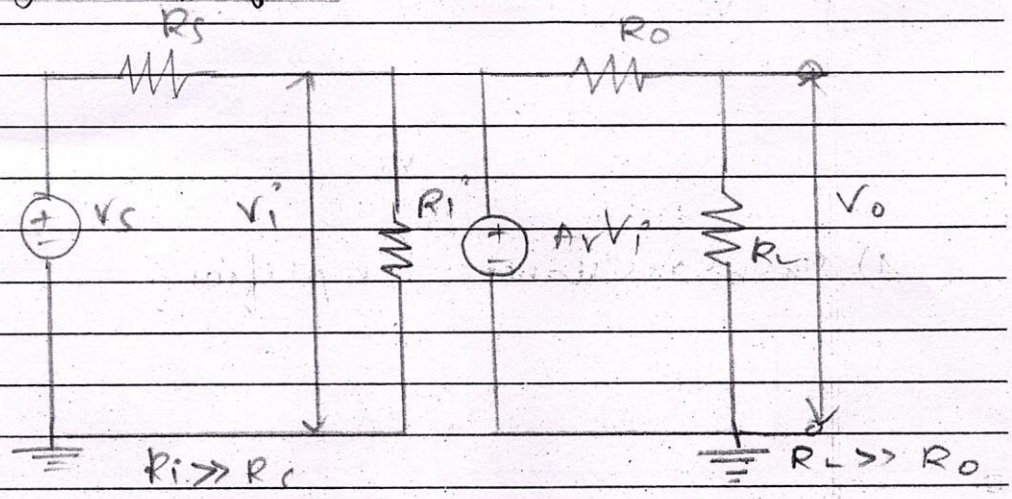
6)

a) Different types of amplifiers circuits:

The amplifier can be classified into four broad categories (Types - 2 marks)

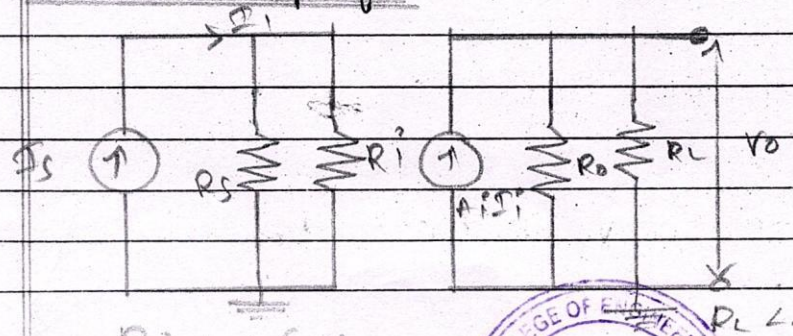
1) Voltage amplifier
2) current amplifier
3) Transconductance amplifier
4) Transresistance amplifier

1) Voltage amplifier :- (Circuit Diagram - 2 marks)



$V_i \simeq V_s$ $V_o = A_v V_i \simeq A_v V_s$

2) Current amplifier: $\rightarrow I_o = I_L$ (circuit Diagram - 2 marks)



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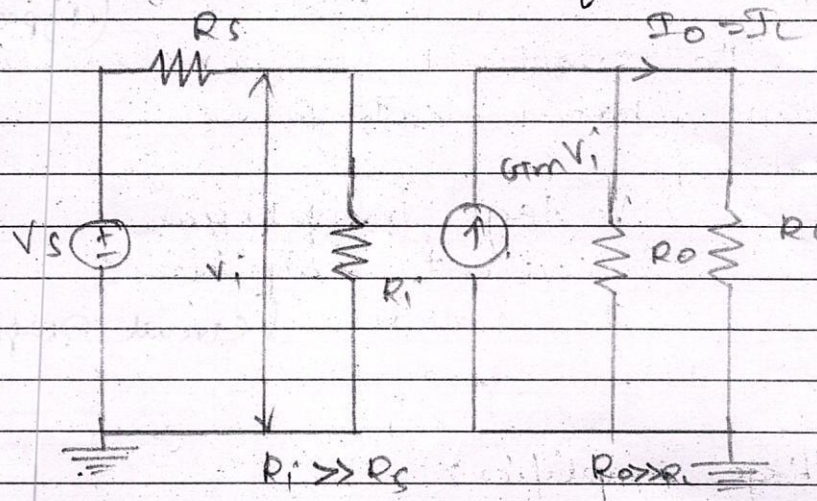
$I_i \simeq I_s \qquad I_L = A_I I_i$

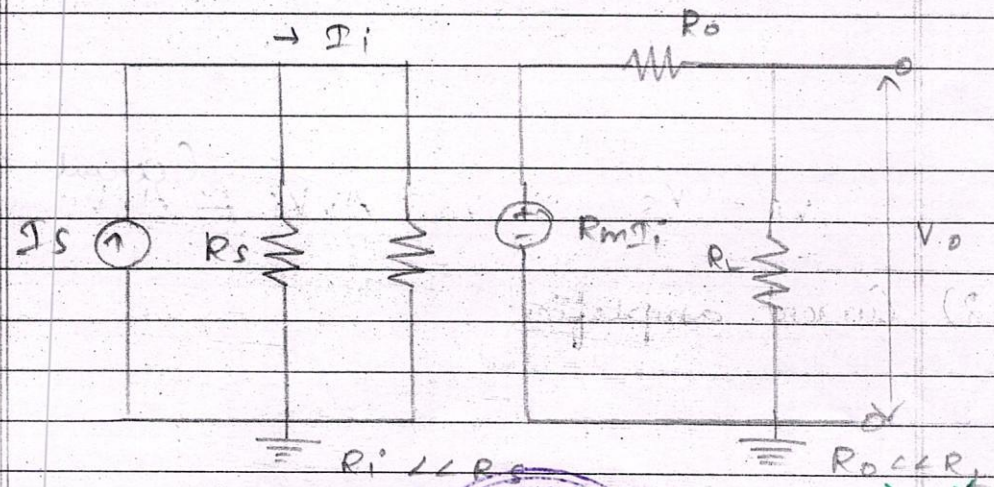
3) <u>Transconductance amplifier:</u> (1 marks)



$V_i \simeq V_s \qquad I_L = G_m V_i$

$$G_m = \frac{I_L}{V_i} = \frac{I_L}{V_s}$$

4) <u>Transresistance amplifier:</u> (1 marks)



$I_i \simeq I_s \qquad V_o = R_m I_i \qquad R_m = \frac{V_o}{I_i}$

$$R_m = \frac{V_o}{I_s}$$

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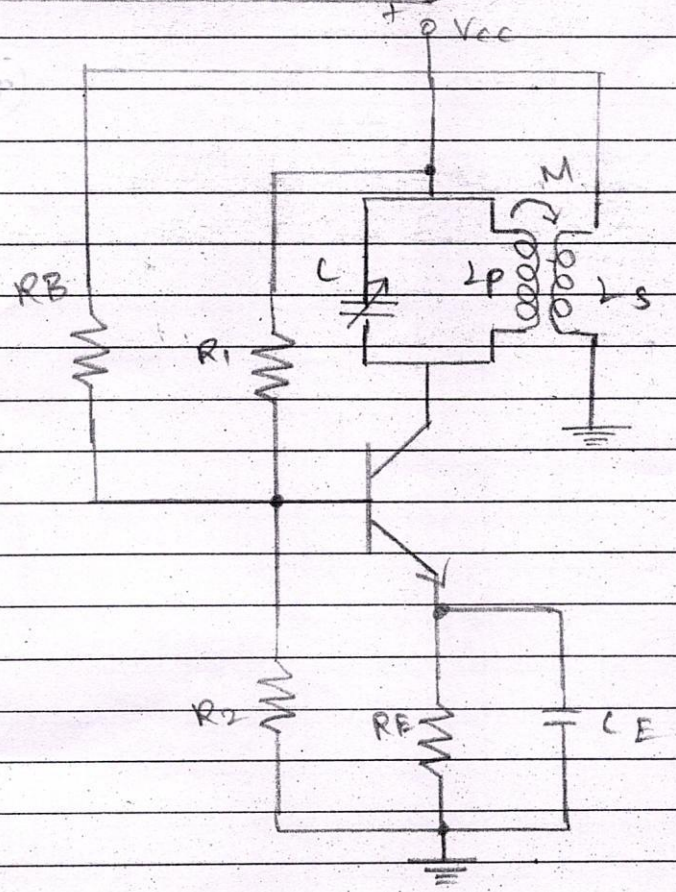
b) Tuned Collector Oscillator: (Diagram - 3 marks)



$$f_0 = \frac{1}{2\pi\sqrt{L_p C}}$$

Clapp oscillator: (equations - 2 marks)

$$f_0 = \frac{1}{2\pi\sqrt{LC}}$$

Where $\frac{1}{C} = \frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3}$

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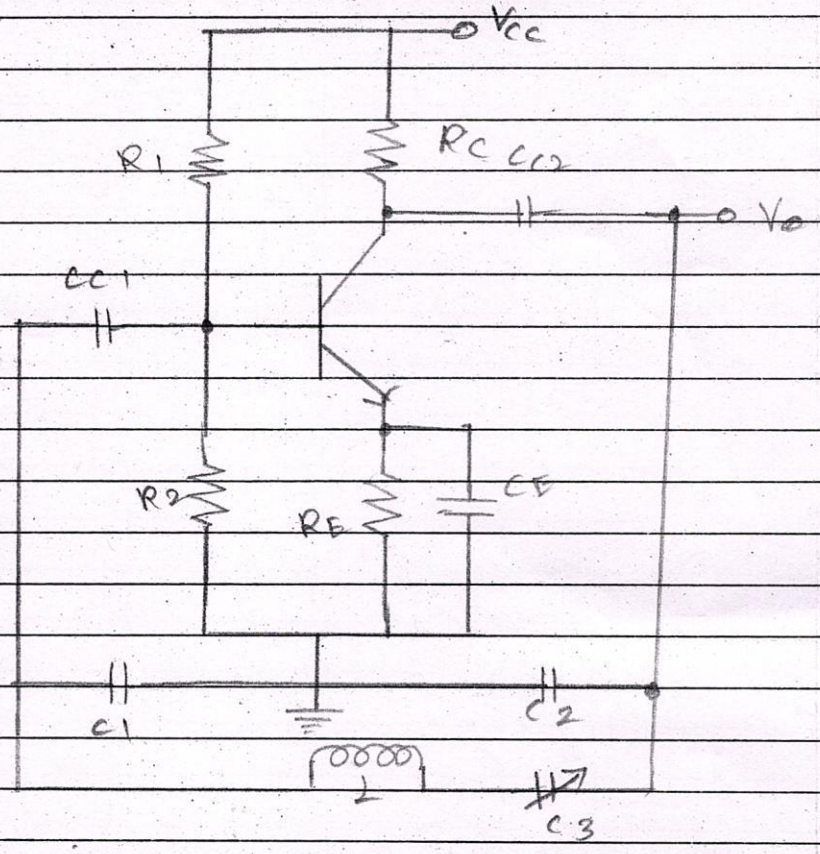
(Diagram - 3 marks)



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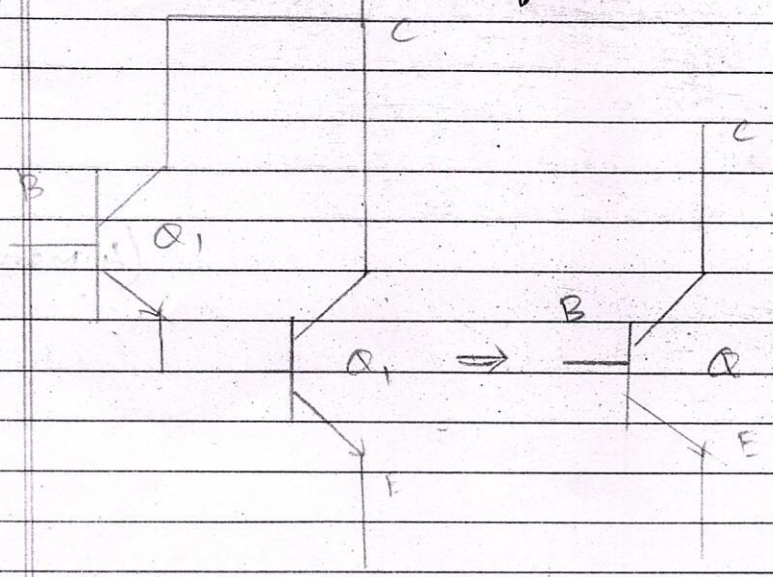
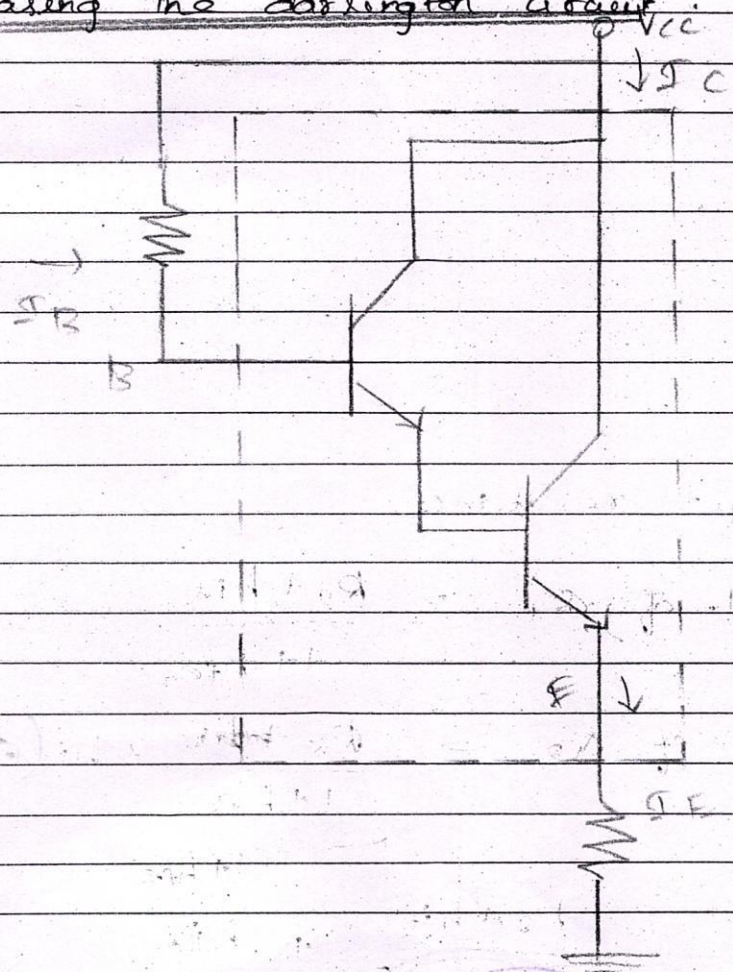
7) a) Darlington amplifier: (4 marks)



(i) Biasing the darlington circuit: (4 marks)



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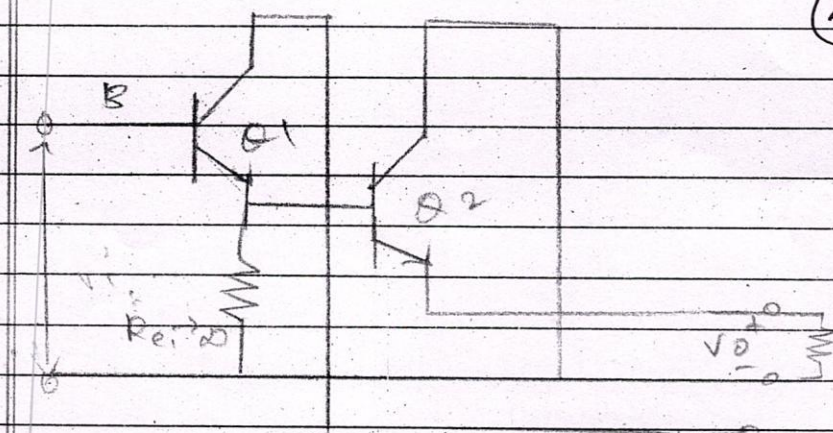
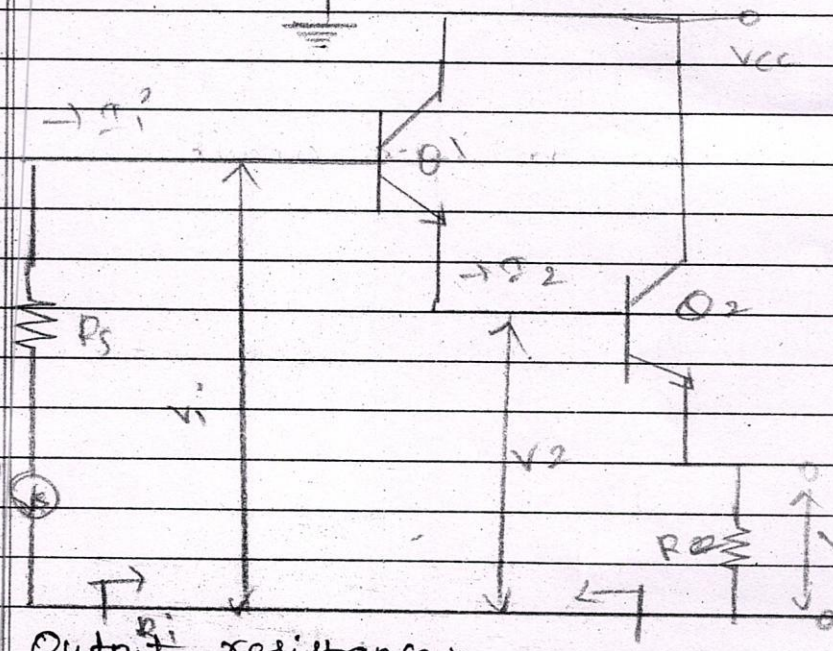
(ii) Darlington composite Emitter follower

(4 marks)





Output resistance:

(Equations-4 marks)

$$R_{o1} \text{ of } Q_1 = \frac{R_s + h_{ie}}{1 + h_{fe}}$$

$$R_{o2} \text{ of } Q_2 = \frac{R_s + h_{ie}}{1 + h_{fe}} + h_{ie}$$

$$= \frac{R_s + h_{ie}}{(1 + h_{fe})} + \frac{h_{ie}}{1 + h_{fe}}$$

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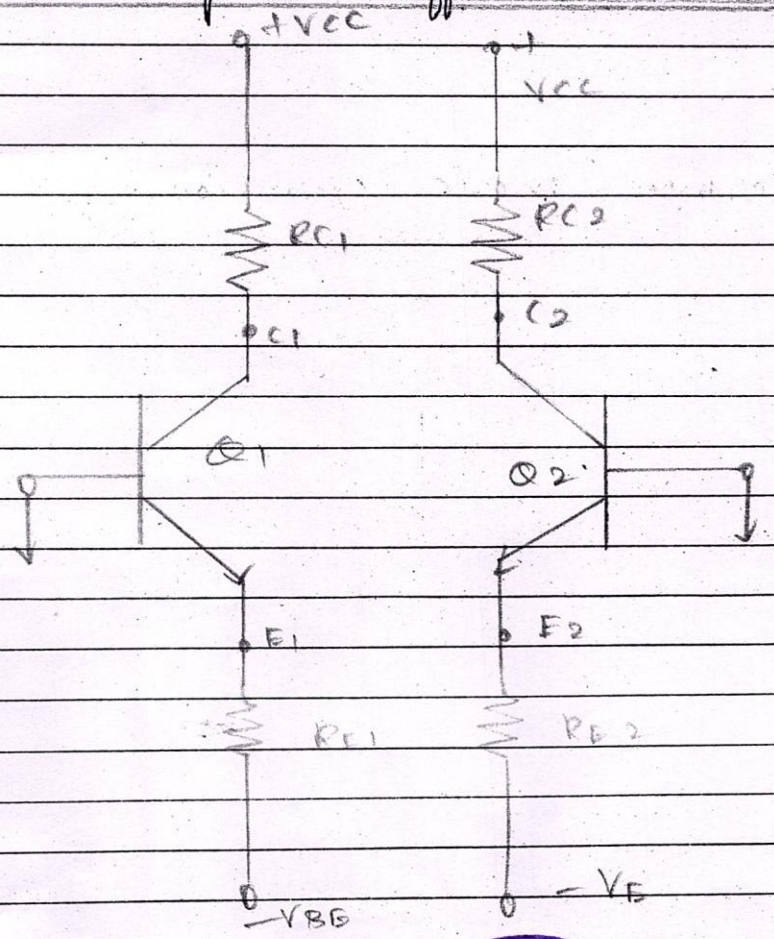
7)

b) Emitter coupled differential amplifier:

Differential amplifier: (2 marks)

* An amplifier which amplifies the difference between two input signals is called differential amplifier.

Emitter coupled differential amplifier: Diagram (6 marks)



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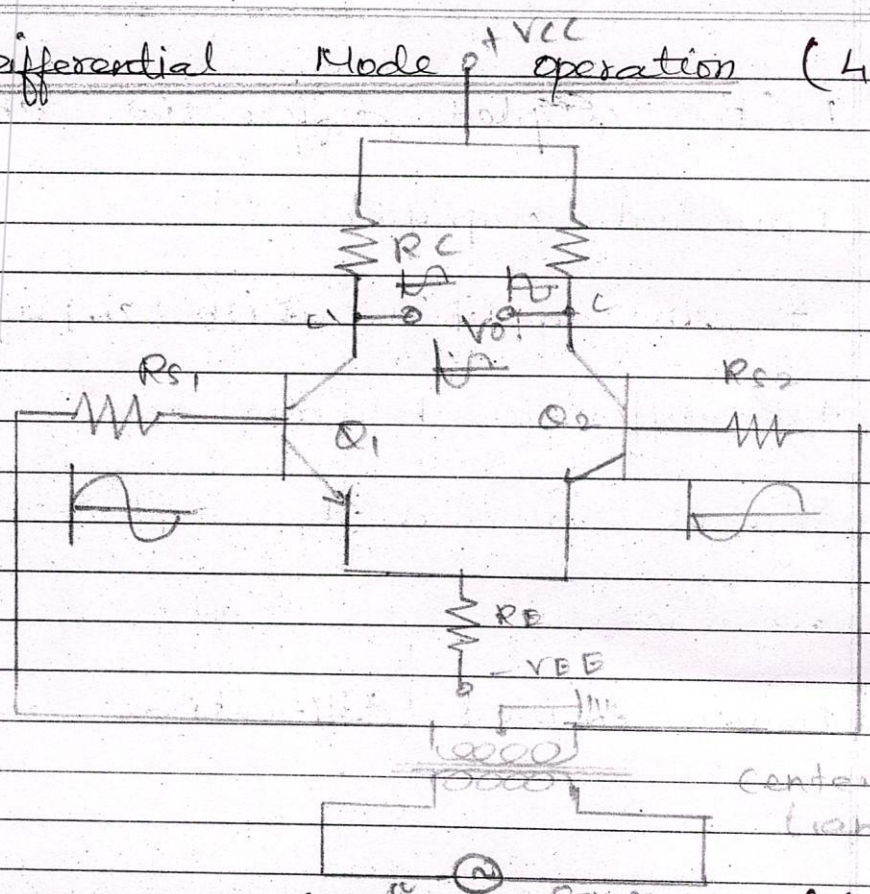
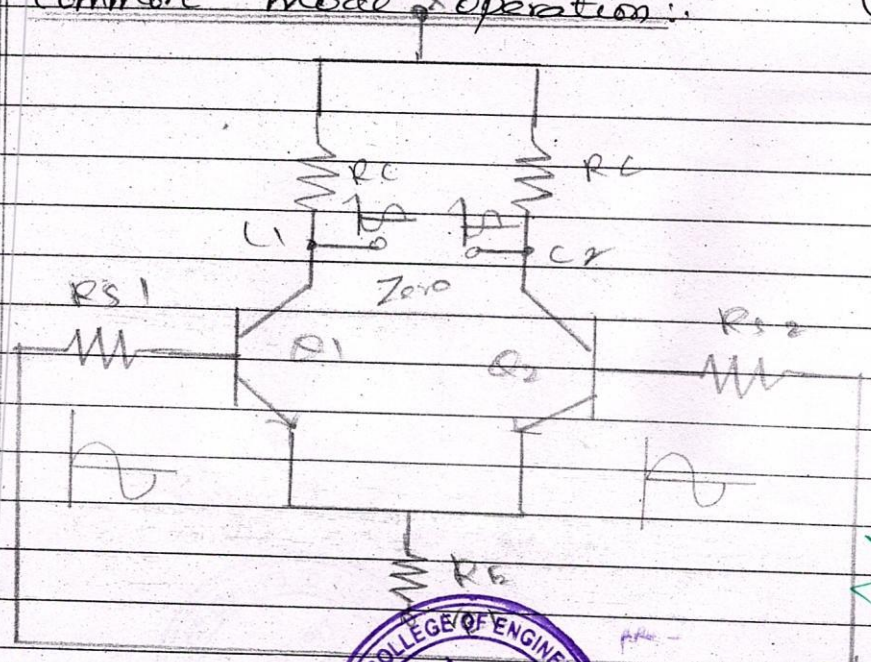
(i) Differential Mode operation (4 marks)



(ii) Common mode operation: (4 marks)



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$Z_2 = C_2 \Rightarrow \frac{1}{j\omega C_2} = \frac{-j}{\omega C_2}$

$Z_3 = L \Rightarrow j\omega L$

General form of LC oscillator:

$h_{ie}(Z_1 + Z_2 + Z_3) + (1 + h_{fe}) Z_1 Z_2 + Z_1 Z_3 = 0$

$h_{ie}\left[\frac{-j}{\omega C_1} - \frac{j}{\omega C_2} + j\omega L\right] + (1 + h_{fe})$

$\left(-j/\omega C_1 - \frac{j}{\omega C_2} - \frac{j}{\omega C_1}\right)(j\omega C) = 0$

$-jh_{ie}\left[\frac{1}{\omega C_1} - \frac{1}{\omega C_2} - \omega L\right] + \frac{1 + h_{fe}}{\omega^2 C_1 C_2} + \frac{L}{C_1} = 0$

$-j h_{ie}\left[\frac{\omega C_2 + \omega C_1 - \omega^3 L C_1 C_2}{\omega^2 C_1 C_2}\right] - \frac{1 + h_{fe}}{\omega^2 C_1 C_2} + \frac{L}{C_1} = 0$

$$\frac{-jh_{ie}\left[\omega C_2 + \omega C_1 - \omega^3 L C_1 C_2\right] - \left[1 + h_{fe}\right] + \omega^2 C_2}{\omega^2 C_1 C_2} = 0$$

equal to zero in imaginary part

$\omega C_2 + \omega C_1 - \omega^3 L C_1 C_2 = 0 \quad [\omega = 2\pi f]$

$\omega C_2 + \omega C_1 = \omega^3 L C_1 C_2$

$\omega(C_2 + C_1) = \omega^3 L C_1 C_2$

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$$C_2 + C_1 = \omega^2 L C_1 C_2$$

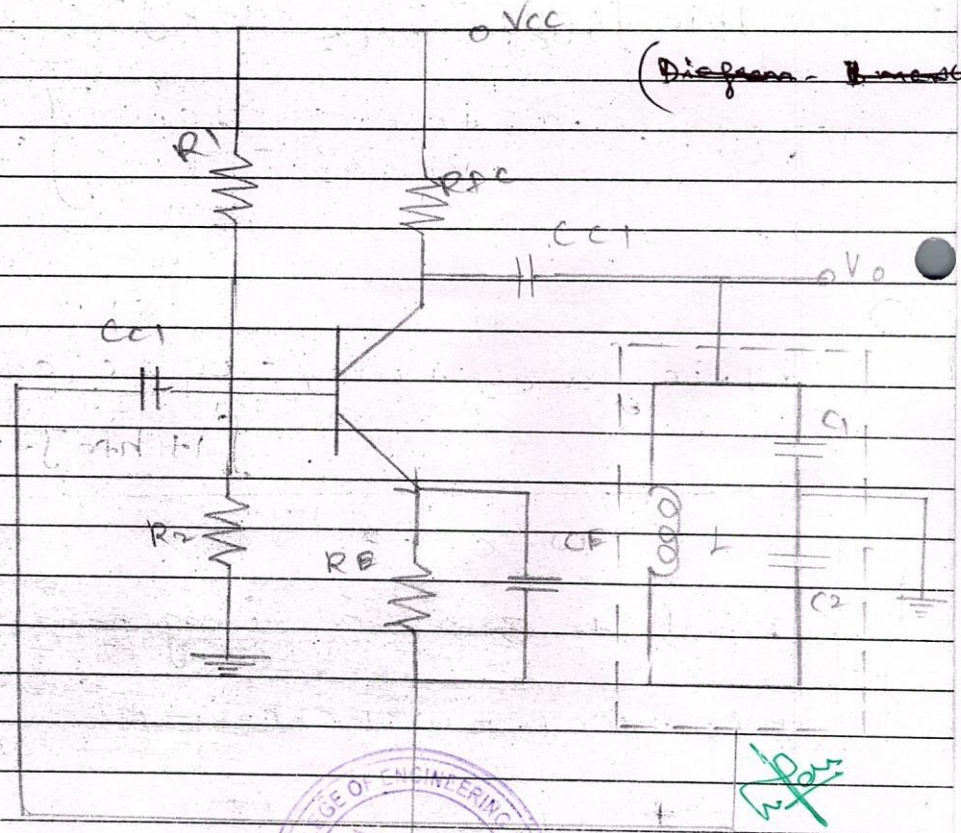
$$\omega^2 = \frac{C_2 + C_1}{L C_1 C_2}$$

$$\omega = \sqrt{\frac{C_2 + C_1}{L C_1 C_2}}$$

$$2\pi f = \frac{\sqrt{C_2 + C_1}}{\sqrt{L C_1 C_2}}$$

$$f = \frac{1}{2\pi}\sqrt{\frac{C_2 + C_1}{L C_1 C_2}}$$

(~~Diagram - 8 marks~~)



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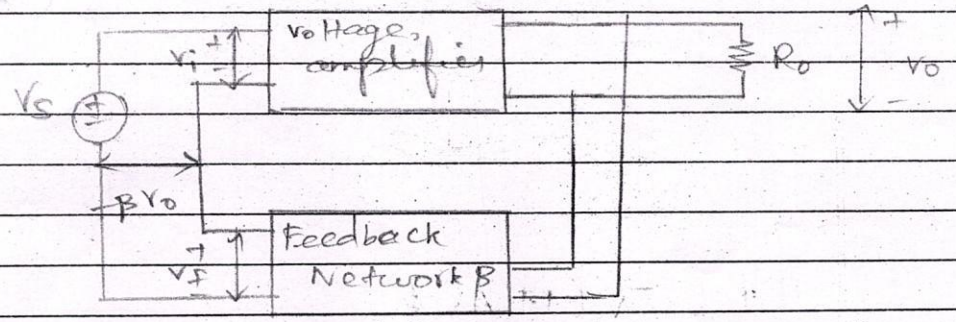
PART – B

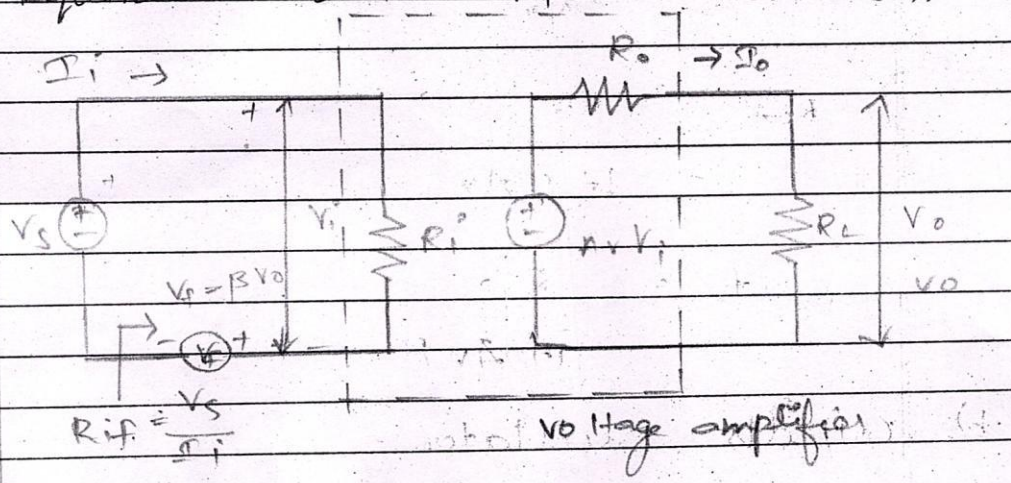
8)

(Diagram - 3 marks)

a) Voltage Series feedback amplifier [Series Shunt]



Equivalent circuit:- (Input Resistance).



$V_s = I_i R_i + \beta V_o$

(Equations - 2 marks)

$V_o = A_v . I_i R_i$

$R_{if} = R_i (1 + \beta A_v)$

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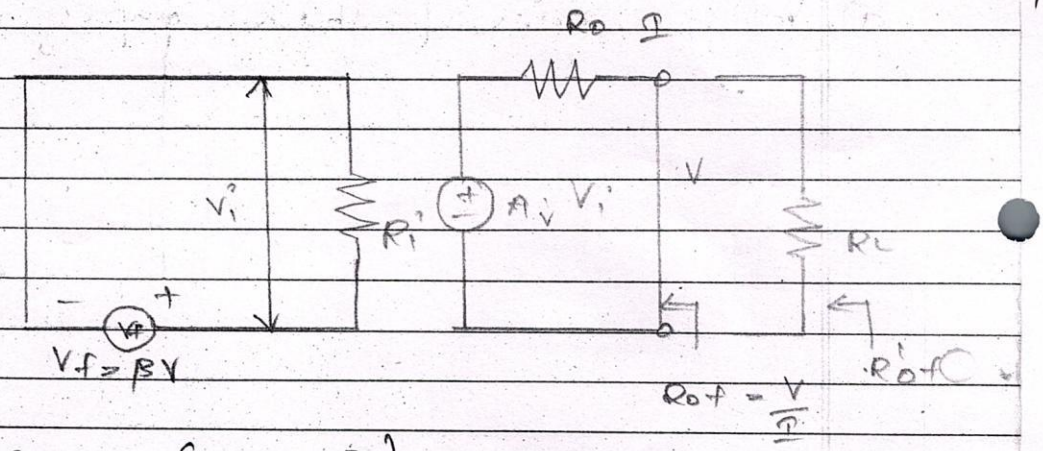
Equivalent circuit :- (Output resistance)

(Diagram - 3 marks)



$$I = \frac{V(1 + A_v \beta)}{R_o}$$

$$R_{of} = \frac{R_o}{1 + \beta A_v}$$

$$R'_{of} = \frac{R'_o}{1 + A_v \beta}$$

b). Colpitts Oscillator :-

Frequency Oscillator :-

(Equations - 5 marks)

$$f = \frac{1}{2\pi\sqrt{L C_{eq}}}$$

$$C_{eq} = \frac{C_1 C_2}{C_1 + C_2}$$

$$Z_1 = C_1 = \frac{1}{j\omega C_1}$$

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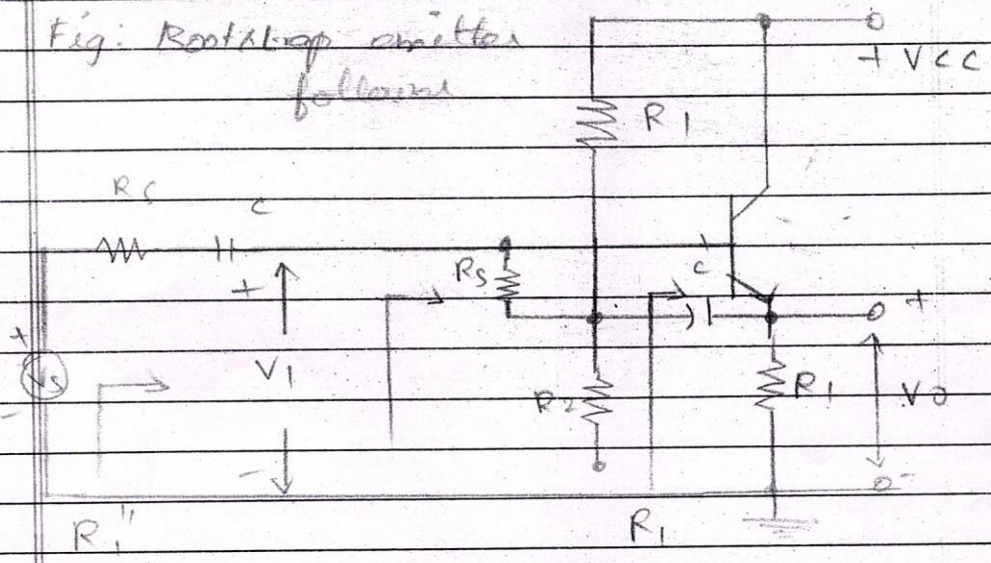
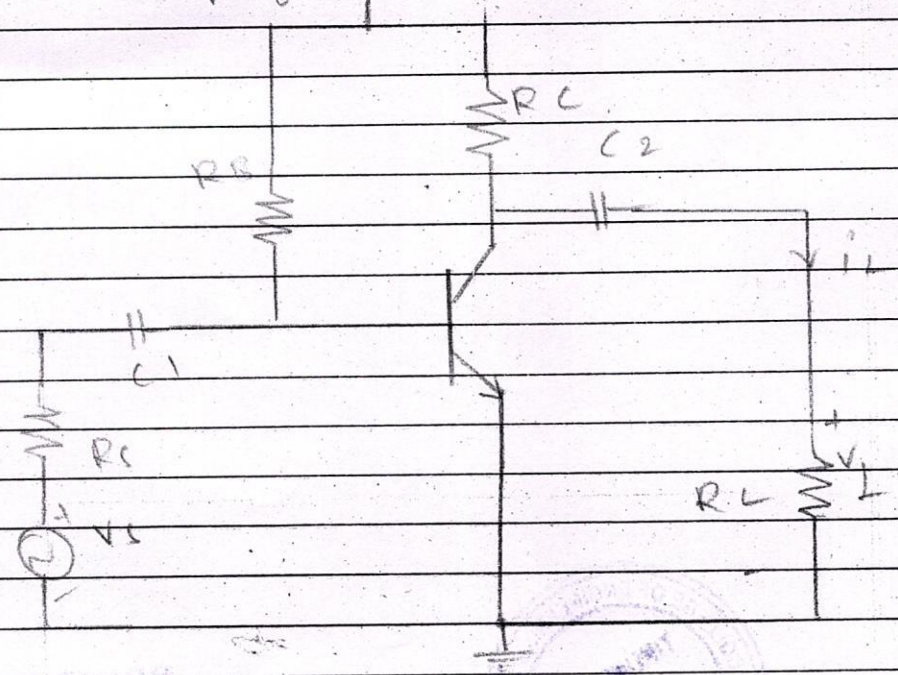
Boot Strap Technique :

(marks-3)

Fig: Bootstrap emitter follower



a) CE amplifier



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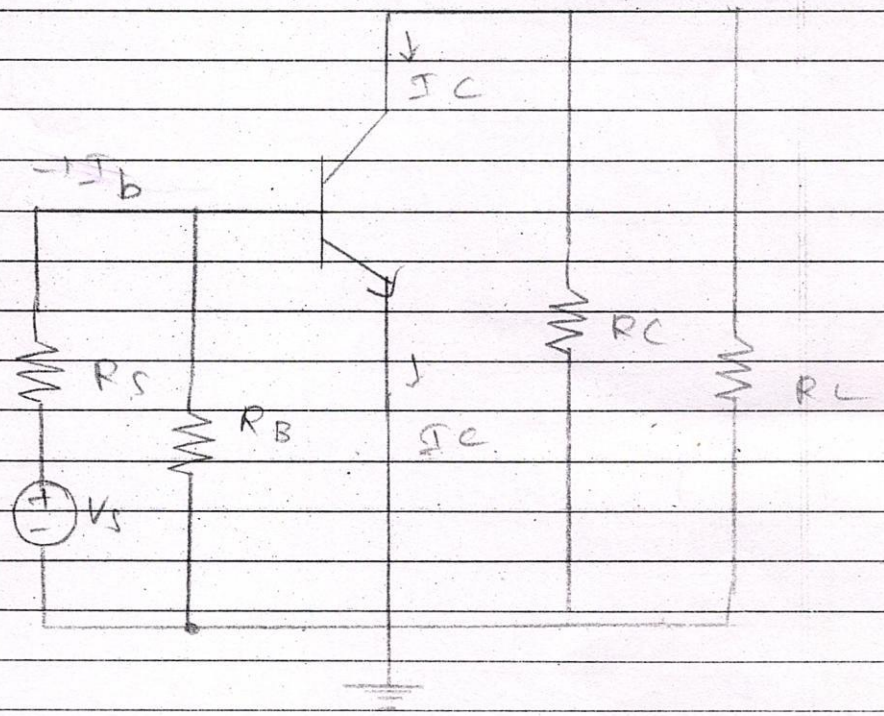
b) AC equivalent circuit



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CONTINUOUS ASSESSMENT TEST

I	II	III
	✓	

Register Number	1	8	0	5	0	0	8

Serial Number	

Degree & Branch	B.E ECE	Semester	IV
Subject Name	Electronic Circuits	Subject Code	
Date & Session	4/2/20, FN	No. of pages used	

Invigilator	Name	M. Jesudin	Signature	

Part - A		Part - B & Part - C					
Question No.	Marks	Question No.		Marks i	Marks ii	Marks iii	Total
1.	2	6.	a.	6			6
2.	2		b.				
3.	2	7.	a.				
4.	1		b.	14			14
5.	2	8.	a.	15			15
			b.				
Total	9						35
Grand Total			44	Max. Marks	50		88/100

Date	Name of the Examiner	Signature of the Examiner
6/2/2020	K. ANITHA	K. Anitha 6/2/2020

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Part - A.

1. Darlington Amplifier:

Darlington Amplifier is made up of PNP or NPN transistors and it disconnecting together to improve input impedance. The transistor emitter is connected to base of ~~the another~~ amplifier. The emitter of PNP transistor is connected to the base of another PNP transistor to create a sensitive transistor with high current gain used in many application

2.

Differential	Common mode gain
* The gain with which it amplifies the difference between 2 i/p ~~impedance~~ signal is called differential amplifier	* The gain with which it amplifies the common mode gain ~~with~~ which produce the output is called common mode gain
* $A_d = \frac{V_o}{V_d}$	* $A_c = \frac{V_o}{V_d}$
* $V_d = V_1 - V_2$	* $V_c = \frac{V_1 + V_2}{2}$
* High differential voltage gain	* Low common mode gain

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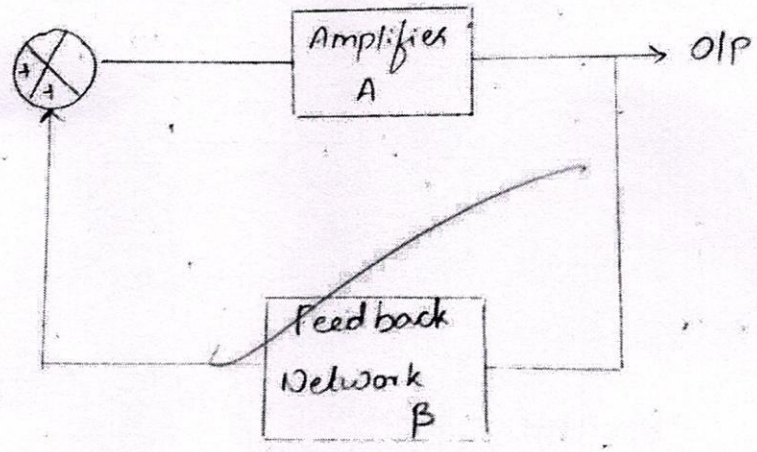
3. Hartley oscillator:

Hartley oscillator is a LC oscillator which has two inductive reactants and one capacitive reactants in the feed back Network.

$$f = \frac{1}{2\pi\sqrt{C(L_1+L_2)}}$$

4. General feed back Structure:



5. Conditions of an Oscillator:

The Conditions of the Oscillator.

The total phase shift in the closes loop is 0° or 360°

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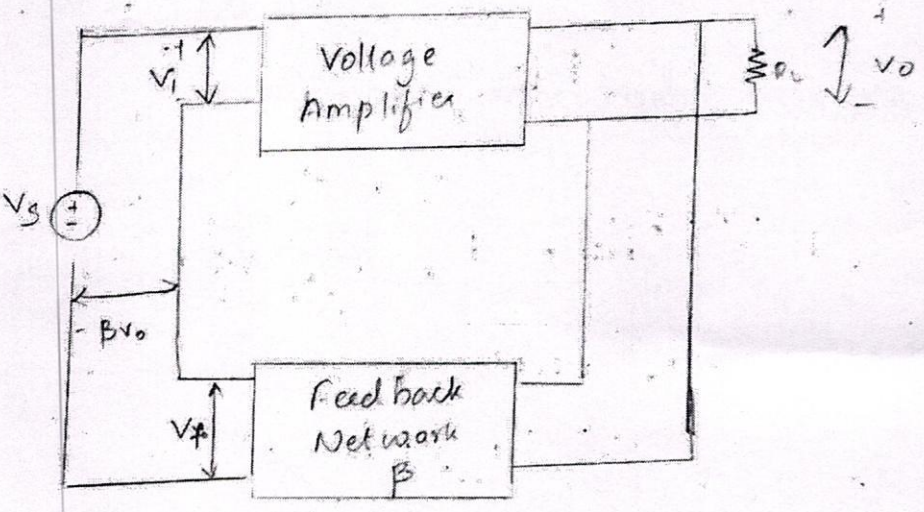
The magnitude of the loop gain is $|A\beta| = 1$

Part - C

8.

a. Voltage Series Amplifier



Voltage series feed back Amplifier is also called as Series Shunt

The Series Shunt feedback name implies.

The Input side the mixer circuit voltage source is used and output side Parallel connect of sample network is used.

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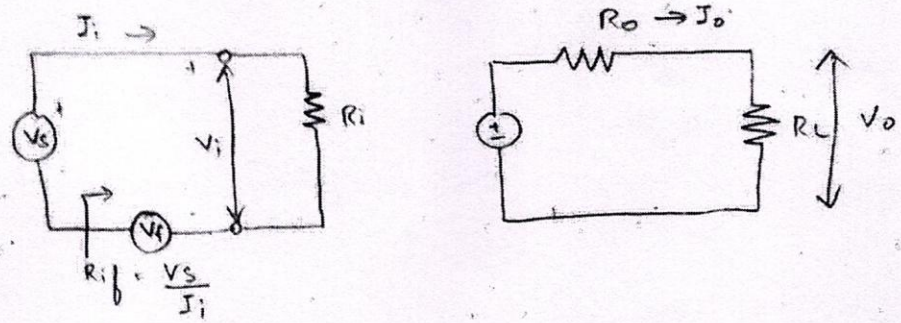
Input Resistance:

Step 1: Draw the equivalent circuit



$$R_{if} = \frac{V_s}{I_i} \quad - (1)$$

Step 2: Obtain Expression for V_s

$$V_s = I_i R_i + V_f$$

$$V_s = I_i R_i + \beta V_o \quad - (2)$$

Step 3: Obtain Expression for V_o in term of I_i

$$V_o = \frac{A_V V_i R_L}{R_o + R_L} = A_V V_i$$

$$V_o = A_V I_i R_i \quad - (3)$$

$$A_V = \frac{A_V R_L}{R_o + R_L}$$

Step 4: Expression for R_{if}

from eqn (1)

$$R_{if} = \frac{V_s}{I_i}$$

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From eqn ②

$$V_S = I_i R_i + \beta V_o$$

Sub eqn ③

$$V_S = I_i R_i + \beta A_V I_i R_i$$

$$V_S = I_i R_i (1 + \beta A_V) \quad - ④$$

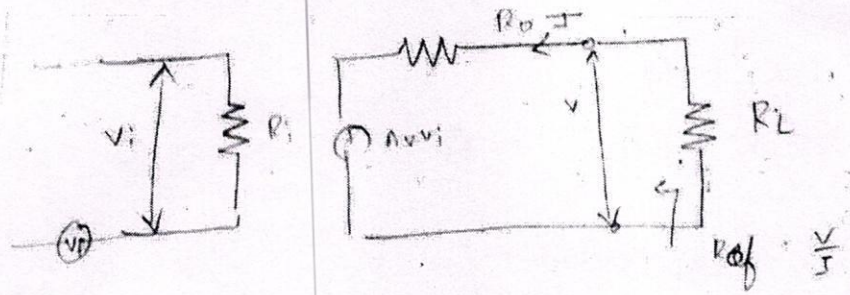
Sub eqn ④ in ①

$$R_{if} = \frac{I_i R_i (1+\beta A_V)}{I_i} \qquad R_f = \frac{V_S}{I_i}$$

$$R_{if} = R_i (1 + \beta A_V) \quad - ⑤$$

O/p resistance.

Step 1: Draw the circuit



Step 2: obtain Exp I in term of V

Apply KVL to O/P side

$$A_V V_i + I R_o = V$$

$$I = \frac{V - A_V V_i}{R_o} \quad ①$$

$$V_i = -V_f = -\beta V_o$$

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$$I = \frac{V - A_V(-\beta V)}{R_o}$$

$$= \frac{V + A_V \beta V}{R_o}$$

$$I = \frac{V(1 + A_V\beta)}{R_o} \quad -(2)$$

Step 3: Obtain Exp for R_{of}

$$R_{of} = \frac{V}{I}$$

$$= \frac{V}{V\left(\frac{1 + A_V\beta}{R_o}\right)} = \frac{R_o}{1 + \beta A_V}$$

Step 4: Expression for R'

$$R'_{of} = R_{of} \parallel R_L$$

$$= \frac{R_{of} R_L}{R_{of} + R_L}$$

$$= \frac{\frac{R_o}{1 + \beta A_V R_L} \cdot R_L}{\frac{R_o}{1 + \beta A_V} + R_L}$$

$$= \frac{\frac{R_o R_L}{1 + \beta A_V}}{\frac{R_o + R_L + \beta A_V R_L}{1 + \beta A_V}}$$

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$$= \frac{R_o R_L}{R_o + R_L + \beta A_V R_L}$$

Divide numerator & Denominator by $R_o + R_L$

$$R'_{of} = \frac{R_o'}{1 + \frac{\beta A_V R_L}{R_o + R_L}}$$

$$R_o' = \frac{R_o R_L}{R_o + R_L}$$

15

7. Emitter couple differential amplifier

The transistor of differential amplifier bases and the emitter bias circuit with which they are indentical in characteristics

Two transistor Q_1 Q_2 are exactly matched characteristic so $R_{C_1} = R_{C_2}$, $R_{E_1} = R_{E_2}$, $|V_{CE}| = |-V_{CC}|$

Differential amplifier is achieched by connecting emitter E_1 (Q_1) and Emitter E_2 (Q_2) due to the R_{E_1}/R_{E_2} and they combined by a single Resistance R_E.

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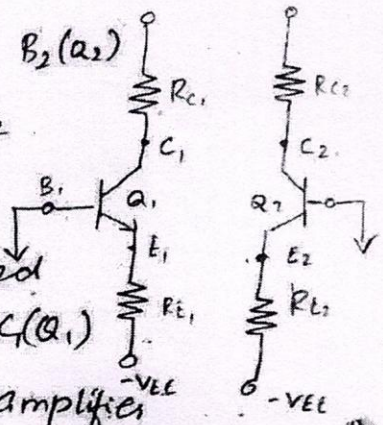
The base $B_1(Q_1)$ is connected to the i/p one which is V_{S1}, similarly $B_2(Q_2)$ is connected to i/p two which is V_{S2}. The supply voltages are measured with respect to ground. The balanced o/p is taken between the collector $C_1(Q_1)$ and the collector $C_2(Q_2)$ such as amplifier is called Emitter couple differential amplifier. The two collector Resistance are same. hence it can be denoted by R_C



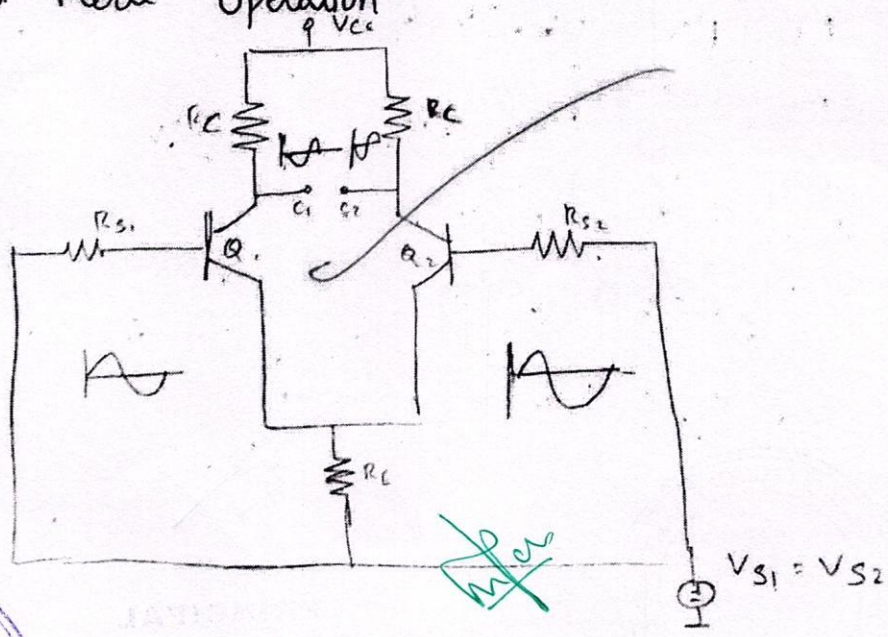
Operation

It is divided into 2 mode

~~Collect~~ * Common mode operation

* Differential mode operation

Common Mode Operation



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In this mode the signals applied to ~~Differential~~ the base of Q_1 & Q_2 are derived from same source. So the two signals are equal in magnitude as well as in phase.

In phase signal voltage at the bases of Q_1 & Q_2 causes in phase signal voltages to appear across R_E which add together R_E carries a signal current and provide negative feedback. This feedback reduces the common mode gain of differential amplifier.

The two signal cause in phase signal voltage of equal magnitude to appear across the two collector of Q_1 & Q_2. Now the output voltage is the difference between the two collector voltages which are equal and same in phase. So V_o is almost zero. negligibly small. Identically it should be zero

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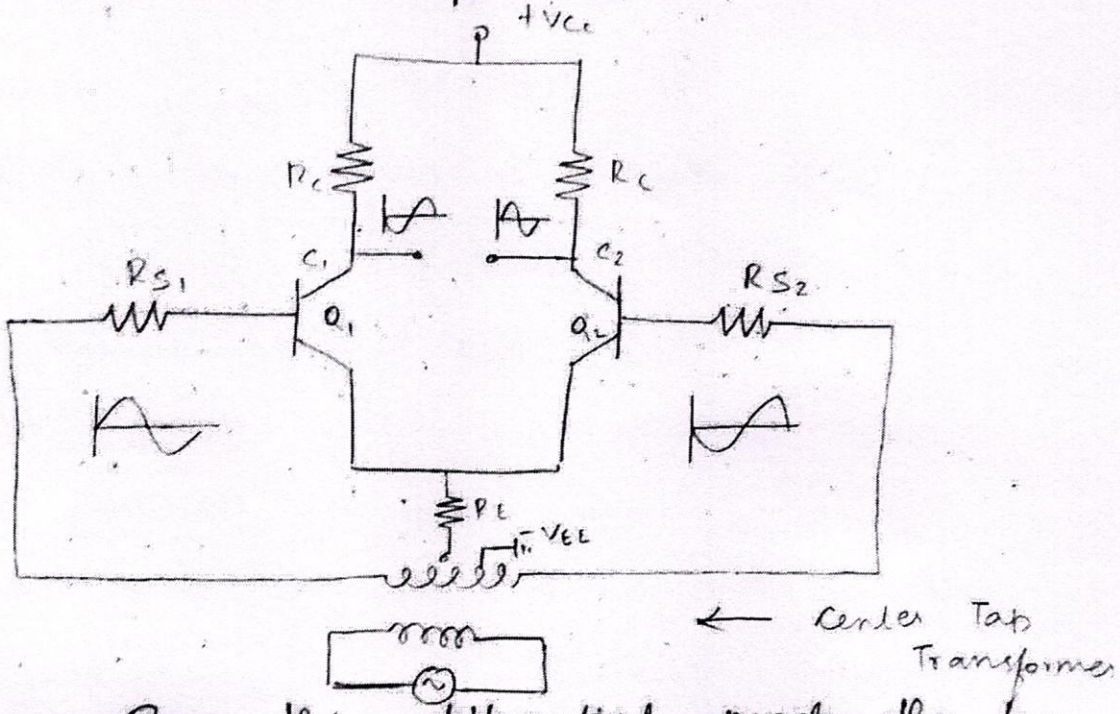
Differential mode operation



In the differential mode the two input signals are different from each other.

Consider two i/p signal which are same in magnitude but 180° out of phase. These signals with opposite phase can be obtained from the center tap transformer.

The sine wave on the base of Q_1 is positive going one. an amplified negative going signal develops on the collector of Q_1

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Due to this a negative going wave is developed across R_E.

In matched pair of transistor voltage across R_E are equal in magnitude but opposite phase due to effect of Q_1 & Q_2.

These two signal cancel each other and there is no signal across the emitter resistance. Hence there is no ac signal current flowing through R_E. So R_E does not produce negative feedback.

V_o is the output taken across collector Q_1 and collector of Q_2. The two output on collector 1 and 2 are equal in magnitude but opposite in polarity. V_o is twice as large as the signal voltage from either collector to ground

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Part - B

6.
a.

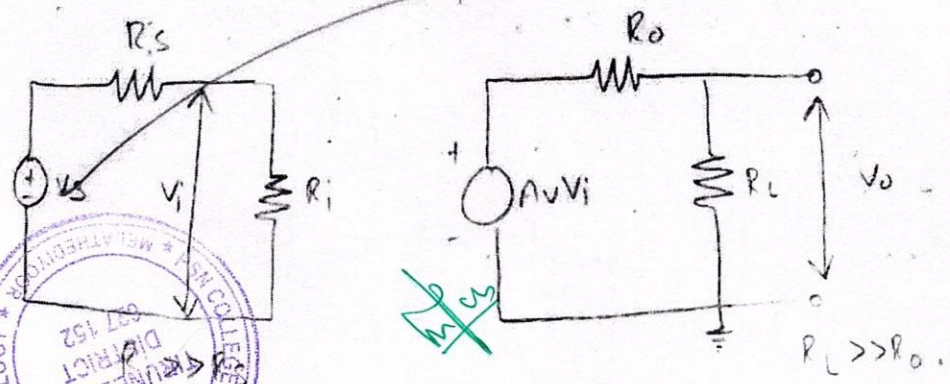
Voltage Amplifier
Current Amplifier
Transconductance Amplifier
Trans resistance Amplifier

Voltage Amplifier.

An amplifier circuit provides a voltage o/p proportional to the voltage input and the proportionality factor does not independent on the magnitude of source and the load ~~impedo~~ resistance is called voltage Amplifier

$V_i \simeq V_s$

$V_o = A_v V_i = A_v V_s$



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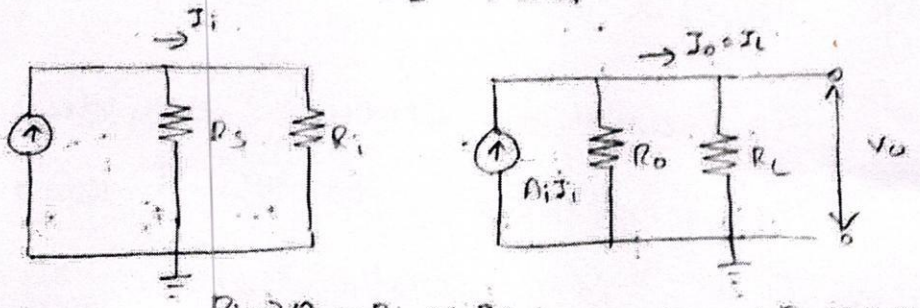
Current Amplifier:

An amplifier circuit provides a current o/p proportional to the current input and the proportionality factor does not independent on the magnitude of source and the load resistance is called current Amplifier

$$I_i \simeq I_s$$

$$I_L = A_I I_i$$

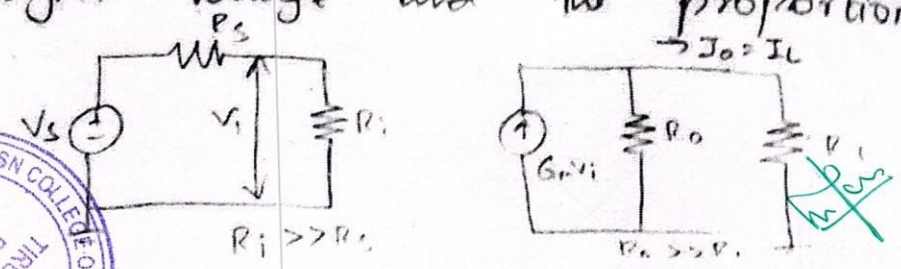


$R_i \to 0$ or $R_i << R_s$ $\qquad$ $R_L << R_o$ or $R_o \to \infty$

Trans Conductance Amplifier:

Trans conductance Amplifier have an Thevenin's equivalent in its input circuit and Norton's in its o/p circuit.

Here o/p circuit proportional to i/p signal voltage and the proportional factor is



$R_i >> R_s$ $\qquad$ $R_o >> R_L$

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independent of magnitude source and load resistance

$$V_i \simeq V_s$$

$$I_L = G_m V_i$$

$$G_m = \frac{I_L}{V_i} = \frac{I_L}{V_s}$$

Transresistance :

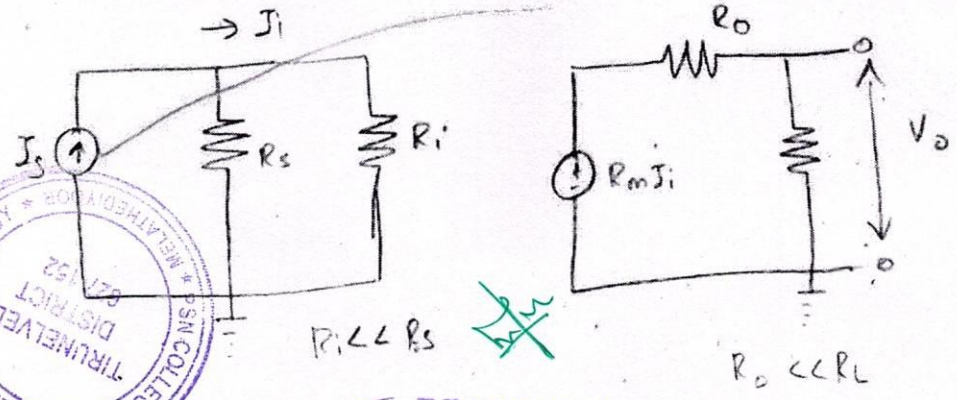
Transresistance amplifier have an Norton's equivalent to its i/p circuit and Thevenin's equivalent to its o/p circuit.

Here o/p voltage proportional to i/p signal voltage and proportional factor is independent of magnitude source and load resistance

$$I_i \simeq I_s$$

$$V_o = R_m I_i$$

$$R_m = \frac{V_o}{I_i} = \frac{V_o}{I_s}$$



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Parameter	Types			
	voltage	current	Transconductance	Trans resistance
i/p resistance	∞	0	∞	0
o/p resistance	0	∞	∞	0
Transfer gain	$A_v = \frac{V_o}{V_s}$	$A_i = \frac{I_L}{I_s}$	$G_m = \frac{I_L}{I_s}$	$R_m = \frac{V_o}{I_s}$

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07/02/2020

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CONTINUOUS ASSESSMENT TEST-II RESULT ANALYSIS

CLASS: II ECE **SEM: 04** **TOTAL NO OF STUDENTS: 22**

S.NO	REGISTER NO.	STUDENTS NAME	ELECTRONIC CIRCUITS
1)	1805001	Arjun.A	84
2)	1805002	Arjun Pandian.K	AB
3)	1805004	Gopi.V	80
4)	1805005	Govinda Raj.G	AB
5)	1805006	HariharaSuthan.C	64
6)	1805007	Inbaraj.G	40
7)	1805008	Kaviyadarshini.G	88
8)	1805009	Mahalakshmi.G	96
9)	1805010	Malathi.K	96
10)	1805011	Manjula.K	88
11)	1805012	Mariammal.M	96
12)	1805013	Merlin.T	78
13)	1805014	MonisriDivya.S	66
14)	1805015	Nandhini.M	94
15)	1805016	Pavithra.P	82
16)	1805017	Petchiammal.S	96
17)	1805019	Sathya.P	82
18)	1805020	SelvaVenkatesh.M	60
19)	1805021	Shanmuga Nathan.P	74
20)	1805022	Sushmitha.S	84
21)	1805023	Vasantha Paul.A	80
22)	1805301	Sangeetha. V	AB

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CAT - II (EVEN SEMESTER 2019-2020)

SUBJECT NAME: ELECTRONICS CIRCUITS

YEAR & SEMESTER: II & IV

		CO1	CO2	CO3	CO4	CO5	BL1	BL2	BL3	BL4
CAT-II	Questions		1,2,7	3,4,5,6,8			1,3,5	2,4,6	8	7
	Total Marks		20	30			6	12	16	16

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MODEL PRACTICAL EXAMINATION SCHEDULE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACADEMIC YEAR (2019 – 2020) – EVEN SEMESTER

MODEL PRACTICAL EXAMINATION – MAR/APR 2020

YEAR	SUBJECT	DATE & SESSION
II	505104 ELECTRONICS CIRCUITS AND SIMULATION LABORATORY	06.03.2020 FN&AN
II	505105 LINEAR INTEGRATED CIRCUITS LABORATORY	07.03.2020 FN&AN
III	205107 DIGITAL COMMUNICATION AND NETWORKS LABORATORY	06.03.2020 FN
III	205108 VLSI DESIGN LABORATORY	07.03.2020 FN

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ACADEMIC YEAR (2019 – 2020) – EVEN SEMESTER

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

505104 ELECTRONICS CIRCUITS AND SIMULATION LABORATORY

VENUE: EDC LAB

S.NO.	DEGREE / BRANCH / SEMESTER	DATE OF EXAM	TIME	REGISTER NUMBER	TOTAL NO OF STUDENTS
1	B.E / ECE/ IV SEM BATCH I	06.03.2020	9.30 A.M – 12.30 P.M	1805001, 1805002, 1805004-12	11
2	B.E / ECE/ IV SEM BATCH II	06.03.2020	1.00 P.M – 4.00 P.M	1805013-17, 1805019-23, 1805301	11

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ACADEMIC YEAR (2019 – 2020) – EVEN SEMESTER

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

505105 LINEAR INTEGRATED CIRCUITS LABORATORY

VENUE: EDC LAB

S.NO.	DEGREE / BRANCH / SEMESTER	DATE OF EXAM	TIME	REGISTER NUMBER	TOTAL NO OF STUDENTS
1	B.E / ECE/ IV SEM BATCH I	07.03.2020	9.30 A.M – 12.30 P.M	1805001, 1805002, 1805004-12	11
2	B.E / ECE/ IV SEM BATCH II	07.03.2020	1.00 P.M – 4.00 P.M	1805013-17, 1805019-23, 1805301	11

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ACADEMIC YEAR (2019 – 2020) – EVEN SEMESTER

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

205107 DIGITAL COMMUNICATION AND NETWORKS LABORATORY

VENUE: DSP LAB

S.NO.	DEGREE / BRANCH / SEMESTER	DATE OF EXAM	TIME	REGISTER NUMBER	TOTAL NO OF STUDENTS
1	B.E / ECE/ VI SEM	06.03.2020	9.30 A.M – 12.30 P.M	1705001 – 09, 11 – 16, 301, 302	17

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ACADEMIC YEAR (2019 – 2020) – EVEN SEMESTER

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

205108-VLSI DESIGN LABORATORY

VENUE: DSP LAB

S.NO.	DEGREE / BRANCH / SEMESTER	DATE OF EXAM	TIME	REGISTER NUMBER	TOTAL NO OF STUDENTS
1	B.E / ECE/ VI SEM	07.03.2020	9.30 A.M – 12.30 P.M	1705001 – 09, 11 – 16, 301, 302	17

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B.E- MODEL PRACTICAL EXAMINATION, MAR / APR 2020

B.E– ELECTRONICS AND COMMUNICATION ENGINEERING

Semester – IV

505104 -ELECTRONICS CIRCUITS AND SIMULATION LABORATORY

Regulation–2018

Time: 3 Hours

Max. Marks 100

Aim & Procedure (20)	Circuit Diagram (50)	Graph & Result (20)	Viva (10)	Total (100)

S.No	LIST OF QUESTIONS	
1	Design a series shunt feedback amplifier and calculate the bandwidth for with feedback using transistors	k3
2	Design a series shunt feedback amplifier and calculate the bandwidth for without feedback using transistors	k6
3	Construct the following circuit and obtain the corresponding output if the input is a sine wave of 12 $V_{(p-p)}$ (i) Positive clipper (ii) Negative Clipper	k5
4	Construct the following circuit and obtain the corresponding output if the input is a sine wave of 12 $V_{(p-p)}$ (i) Integrator (ii) Differentiator	k5
5	Design & Construct RC Phase shift Oscillator to generate the waveform using transistor	k3
6	Design & Construct Weinbridge Oscillator to generate the waveform using transistor	k3
7	Design and construct a Bistable multivibrator using transistor	k4
8	Design and construct a Monostable multivibrator using transistor	k4
9	Construct the following circuit and obtain the corresponding output if the input is a sine wave of 12 $V_{(p-p)}$ (i) Positive clamper (ii) Negative Clamper	k5

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10	Design and construct a RC Coupled power amplifier	K4
11	Design and construct a Class A Coupled power amplifier	K4
12	Design and simulate an Astable multivibrator to generate the square waveform using PSPICE	K3
13	Design and simulate the positive and negative clipper circuit and obtain the corresponding output using PSPICE	K3
14	Design and simulate a bootstrap amplifier using PSPICE	K3
15	Design and simulate LPF and HPF using PSPICE	K3
16	Design ~~and simulate~~ a CMOS Inverter using PSPICE	K6
17	Design and simulate a NAND gate using PSPICE	K3
18	Design and simulate a NOR gate using PSPICE	K3
19	Design ~~and simulate~~ a Differential amplifier using difference mode using PSPICE	K6
20	Design ~~and simulate~~ a Differential amplifier using common mode using PSPICE	K6
21	Design and simulate the positive and negative clamper circuit and obtain the corresponding output using PSPICE	K4
22	Design and construct a Single tuned amplifier using transistor	K4
23	Design ~~and simulate~~ a Hartley oscillator using PSPICE	K6
24	~~Design and~~ simulate a Colpitts oscillator using PSPICE	K2
25	~~Design and~~ simulate a ADC and DAC using PSPICE	K2

INTERNAL EXAMINER

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MELATHEDIYOOR

PSN GROUP OF INSTITUTIONS

Name : B. Sattanathan Department: EIE

Register No : 1806005 Semester: IV

Subject : Analog and digital Integrated circuits

Name of the College: PSN CET

Certified that this Bonafied Record of the work done by the above Student of the ANALOG AND DIGITAL INTEGRATED CIRCUITS *laboratory during the year* 2019 - 220

14/3/2020

Signature of Lab-in-charge

14/03/2020

Signature of Head of Dept.

Submitted for the practical Examination held on 16.03.2020

16/3/2020

Internal Examiner

16/3/20

External Examiner

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16. Generate the waveform to the following functions using MATLAB programming (100)

i) Sine ii) Exponential iii) Cosine iv) Unit impulse v) Unit step

Aim	Algorithm	Program	Output verification	Result & graph	Record	Viva	Total
10	10	40	20	5	5	10	100
10	10	40	20	5	5	8	98

MODEL EXAM

ROLL NO :- 1705002

BRANCH :- II - YEAR - ECE

COLLEGE :- PSNCET

DATE :- 26.09.2019

Analog & DSP lab

AIM :-

To write the continuous and discrete signal (sine, Exponential, cosine, unit impulse, unit step) using MATLAB

APPARATUS REQUIRED :-

HARDWARE :- Personal computer

SOFT WARE :- MATLAB version.

PROCEDURE :-

* Start the program
* open M-file
* Type the program
* compile and Run the program.
* If any error occurs in the program, clean all error and another one time Run the program.
* Stop the program

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% program of exponential wave

```
n4 = 0:1:N-1;
y4 = exp(N*n4);
subplot(2,2,1);
stem(n4,y4,'k');
xlabel('Time');
ylabel('Amplitude');
title('exponential wave');
```

% program of unit impulse wave

```
n = -3:1:3;
y = [zeros(1,3), ones(1,1), zeros(1,3)];
subplot(2,2,2);
stem(n,y,'k');
xlabel('Time');
ylabel('Amplitude');
title('unit impulse wave');
```

% program of unit step wave

```
clc;
N= ('Enter the length of unit
        step (N) =')
n = 0:1:N-1
subplot(2,2,3);
y = ones(1,N);
stem(n,y,'k');
xlabel('Time');
ylabel('Amplitude');
title('unit step wave');
```

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PROJECT SCHEDULE

College code and Name: 9522 - PSN College of Engineering and Technology

Year / Semester: IV / 08

Branch: Electronics and Communication Engineering

Subject Code and Name: 205301 - Project Work

Academic Year: 2020-2021(Even)

BATCH NO.	REG. NO.	NAME	PROJECT TITLE	PROJECT GUIDE
1	1705002	ESWARAPANDIAN.K	IOT BASED CCTV IMAGE CAPTURE AND NOTIFICATION FOR SECURITY PURPOSE	Mr.G.ESWARAN AP/ECE
	1705013	SRIRAM.A		
	1705302	UMAR MYDEEN.A		
2	1705009	NITHISHPANDIAN.S	AUTOMATION OF INDUSTRIAL GAS LEAKAGE DETECTION SYSTEM	Mrs.M.SUNDARA MAHALAKSHMI AP/ECE
	1705005	MANIKANDAN. S		
3	1705001	BABY SELVAM. S	HYBRID OPTIMIZATION TECHNIQUE BASED OPTIMAL CHARGING COORDINATION OF ELECTRIC VEHICLES CONSIDERING VEHICLE TO GRID TECHNOLOGY	Dr.K.PAUL JOSHUA HOD/ECE
	1705004	MAHASURIYA. C		
4	1705012	SHINY.G	DEEP LEARNING BASED ENERGY THEFT DETECTION FOR IOT ENABLED SMART HOME	Dr.K.PAUL JOSHUA HOD/ECE
	1705014	SUBBULEKSHMI.M		
5	1705301	GUNABALASINGH.R	RENEWABLE ENERGY BASED SMART E BULK	Mrs.A.CHIERA AP/ECE
	1705008	MUTHU RAJ. B		
6	1705015	SURIYAKAN.S	REMOTELY CONTROLLED ANDROID BASED ELECTRONIC NOTICE BOARD	Ms.K.ANITHA AP/ECE
	1705016	VIGNESH.R		
7	1705007	MUTHUSAIBA. H	DEEP LEARNING BASED MALWARE DETECTION TECHNIQUE FOR ANDROID IOT DEVICES	Dr.K.PAUL JOSHUA HOD/ECE
	1705011	RAMALAKSHMI.S		

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8	1705003	KALA. T	ELECTRIC VEHICLE CHARGING STATION INTEGRATED WITH RES USING STOCHASTIC PLANNING MODEL	Dr.K.PAUL JOSHUA HOD/ECE
	1705006	MANIKKAALAGUSELVI.M		

PROJECT CO-ORDINATOR

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PROJECT REVIEWS & MARKS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

YEAR/SEM: IV/08 **SUBJECT CODE & NAME:** 205301 & PROJECT WORK

REVIEW: 00 **DATE**: 27.01.2021

S.NO	REG. NO	STUDENT NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1.	1705001	BABY SELVAM. S	10	5	3	18
2.	1705002	ESWARAPANDIAN.K	10	10	5	25
3.	1705003	KALA. T	10	7	4	21
4.	1705004	MAHASURIYA. C	10	5	3	18
5.	1705005	MANIKANDAN. S	10	6	2	18
6.	1705006	MANIKKAALAGUSELVI.M	10	5	3	18
7.	1705007	MUTHUSAIBA. H	10	8	5	23
8.	1705008	MUTHU RAJ. B	10	4	4	18
9.	1705009	NITHISHPANDIAN.S	10	4	4	18
10.	1705011	RAMALAKSHMI.S	10	8	5	23
11.	1705012	SHINY.G	10	8	5	23
12.	1705013	SRIRAM.A	10	10	5	25
13.	1705014	SUBBULEKSHMI.M	10	10	5	25
14.	1705015	SURIYAKAN.S	10	4	4	18
15.	1705016	VIGNESH.R	10	4	4	18
16.	1705301	GUNABALASINGH.R	10	6	2	18
17.	1705302	UMAR MYDEEN.A	10	8	5	23

PROJECT CO-ORDINATOR

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

YEAR/SEM: IV/08 **SUBJECT CODE & NAME:** 205301 & PROJECT WORK

REVIEW: 01 **DATE**: 24.02.2021

S.NO	REG. NO	STUDENT NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1.	1705001	BABY SELVAM. S	10	5	5	20
2.	1705002	ESWARAPANDIAN.K	10	8	5	23
3.	1705003	KALA. T	10	6	4	20
4.	1705004	MAHASURIYA. C	10	6	5	21
5.	1705005	MANIKANDAN. S	10	4	4	18
6.	1705006	MANIKKAALAGUSELVI.M	10	6	5	21
7.	1705007	MUTHUSAIBA. H	10	10	5	25
8.	1705008	MUTHU RAJ. B	10	4	4	18
9.	1705009	NITHISHPANDIAN.S	10	6	4	20
10.	1705011	RAMALAKSHMI.S	10	8	5	23
11.	1705012	SHINY.G	10	10	5	25
12.	1705013	SRIRAM.A	10	8	5	23
13.	1705014	SUBBULEKSHMI.M	10	8	5	23
14.	1705015	SURIYAKAN.S	10	6	5	21
15.	1705016	VIGNESH.R	10	5	5	20
16.	1705301	GUNABALASINGH.R	10	4	4	18
17.	1705302	UMAR MYDEEN.A	10	8	5	23

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YEAR/SEM: IV/08 **SUBJECT CODE & NAME:** 205301 & PROJECT WORK

REVIEW: 02 **DATE**: 17.03.2021

S.NO	REG. NO	STUDENT NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1.	1705001	BABY SELVAM. S	10	6	5	21
2.	1705002	ESWARAPANDIAN.K	10	8	5	23
3.	1705003	KALA. T	10	5	5	20
4.	1705004	MAHASURIYA. C	10	6	5	21
5.	1705005	MANIKANDAN. S	10	4	4	18
6.	1705006	MANIKKAALAGUSELVI.M	10	4	4	18
7.	1705007	MUTHUSAIBA. H	10	8	5	23
8.	1705008	MUTHU RAJ. B	10	4	4	18
9.	1705009	NITHISHPANDIAN.S	10	6	4	20
10.	1705011	RAMALAKSHMI.S	10	8	5	23
11.	1705012	SHINY.G	10	8	5	23
12.	1705013	SRIRAM.A	10	8	5	23
13.	1705014	SUBBULEKSHMI.M	10	8	5	23
14.	1705015	SURIYAKAN.S	10	4	4	18
15.	1705016	VIGNESH.R	10	4	3	17
16.	1705301	GUNABALASINGH.R	10	4	4	18
17.	1705302	UMAR MYDEEN.A	10	8	5	23

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

YEAR/SEM: IV/08 **SUBJECT CODE & NAME:** 205301 & PROJECT WORK

REVIEW: 03 **DATE**: 07.03.2021

S.NO	REG. NO	STUDENT NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1.	1705001	BABY SELVAM. S	10	8	5	23
2.	1705002	ESWARAPANDIAN.K	10	10	5	25
3.	1705003	KALA. T	10	8	5	23
4.	1705004	MAHASURIYA. C	10	7	5	22
5.	1705005	MANIKANDAN. S	10	7	3	20
6.	1705006	MANIKKAALAGUSELVI.M	10	9	4	23
7.	1705007	MUTHUSAIBA. H	10	10	5	25
8.	1705008	MUTHU RAJ. B	10	7	5	22
9.	1705009	NITHISHPANDIAN.S	10	6	4	20
10.	1705011	RAMALAKSHMI.S	10	10	5	25
11.	1705012	SHINY.G	10	10	5	25
12.	1705013	SRIRAM.A	10	10	5	25
13.	1705014	SUBBULEKSHMI.M	10	10	5	25
14.	1705015	SURIYAKAN.S	10	8	5	23
15.	1705016	VIGNESH.R	10	8	5	23
16.	1705301	GUNABALASINGH.R	10	5	5	20
17.	1705302	UMAR MYDEEN.A	10	10	5	25

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YEAR/SEM: IV/08 **SUBJECT CODE & NAME:** 205301 & PROJECT WORK

PROJECT REVIEW CONSOLIDATED MARKS

S.NO	REG. NO	STUDENT NAME	ZEROTH REVIEW	FIRST REVIEW	SECONG REVIEW	THIRD REVIEW	TOTAL MARKS
18.	1705001	BABY SELVAM. S	18	20	21	23	82
19.	1705002	ESWARAPANDIAN.K	25	23	23	25	96
20.	1705003	KALA. T	21	20	20	23	84
21.	1705004	MAHASURIYA. C	18	21	21	22	82
22.	1705005	MANIKANDAN. S	18	18	18	20	74
23.	1705006	MANIKKAALAGUSELVI.M	18	21	18	23	80
24.	1705007	MUTHUSAIBA. H	23	25	23	25	96
25.	1705008	MUTHU RAJ. B	18	18	18	22	76
26.	1705009	NITHISHPANDIAN.S	18	20	20	20	78
27.	1705011	RAMALAKSHMI.S	23	23	23	25	94
28.	1705012	SHINY.G	23	25	23	25	96
29.	1705013	SRIRAM.A	25	23	23	25	96
30.	1705014	SUBBULEKSHMI.M	25	23	23	25	96
31.	1705015	SURIYAKAN.S	18	21	18	23	80
32.	1705016	VIGNESH.R	18	20	17	23	78
33.	1705301	GUNABALASINGH.R	18	18	18	20	74
34.	1705302	UMAR MYDEEN.A	23	23	23	25	94

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